

Technical Analysis of Lockbit4.0 Evasion Tales - 0x0d4y Malware Research

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Published: 2025-02-19 · Archived: 2026-04-05 22:05:58 UTC



The [Ransomware-as-a-Service](#) (RaaS) group [Lockbit](#) is the main pillar of this Ransomware business model, largely due to its strong commitment to the development of its product, producing Ransomware with implementations that are up to date on the date of each release. The **Lockbit4.0** (or **Lockbit Green**) version is no different, as it is a major update, especially in the *Evasion* and *Obfuscation* layer. In this research, I will analyze the main **Obfuscation** and **Evasion** capabilities implemented in Lockbit4.0, and some Intelligence insights will be provided after the analysis.

Below is the SHA256 hash of the Lockbit4.0 sample that I will analyze in this research.

```
"sha256": "21E51EE7BA87CD60F692628292E221C17286DF1C39E36410E7A0AE77DF0F6B4B"
```

Reverse Engineering of the Unpacking Process

The *Lockbit4.0* unpacking process is quite complex, and I will try to describe my analysis based on its pseudocode. Below, we can see the beginning of the unpacking algorithm.

```

140125011 void* const __return_addr_1 = __return_addr
140125011
14012501d while (true)
14012501d     char rdx = *arg2
140125020     int32_t temp0_1 = arg5.d
140125020     int32_t temp1_1 = arg5.d
140125020     arg5 = zx.q(arg5.d * 2)
140125020     bool c_1 = temp0_1 + temp1_1 u< temp0_1
140125020

```

The code starts by reading a byte from the compressed stream, storing it in **rdx**. It then loads the value of **arg5** and multiplies it by 2 (**arg5 << 1**). This forces a carry when the most significant bit (**MSB**) is cleared. The carry is saved in the **c_1** flag and will be used later to determine whether the byte can be copied directly or needs to be processed.

```

140125022 if (temp0_1 == neg.d(temp1_1))
140125024     int32_t rbx = *arg2
140125026     char* temp2_1 = arg2
140125026     arg2 -= -4
140125026     bool c_2 = temp2_1 u< -4
14012502a     arg5 = zx.q(adc.d(rbx, rbx, c_2))
14012502a     c_1 = adc.d(rbx, rbx, c_2) u< rbx || (c_2 && adc.d(rbx, rbx, c_2) == rbx)
14012502c     rdx = *arg2

```

The code checks whether **arg5** (in *temp0_1*) is equal to its complement (**-arg5** or *temp1_1*). This is because certain values in the compressed byte stream represent special markers that need to be processed differently. If the equality is true:

1. A new byte is loaded into **rbx**.
2. The **arg2** pointer is adjusted to advance 4 bytes.
3. An **ADC** ([Add with Carry](#)) operation is performed on **rbx**, modifying **arg5**.
4. A new byte is loaded into **rdx**, continuing the data extraction.


```

1401250bf         if (rax_7.b u> 0x8f)
1401250bf         |         goto label_1401250c7
1401250bf
1401250c5         if (*(rsi_3 - 2) != 0xf)
1401250c5         |         goto label_1401250c7
1401250c5
1401250e9
1401250e9     label_1401250e9:
1401250ea     void* lpfl0ldProtect_2 = lpfl0ldProtect
1401250ea     void* rdi_4 = lpfl0ldProtect_2 + 0x122000
1401250f1     uint64_t* rbx_4 = lpfl0ldProtect_2 - 4
1401250f7     uint64_t lpfl0ldProtect_1
1401250f7
1401250f7     while (true)
1401250f7     |         int32_t lpfl0ldProtect_3
1401250f7     |         lpfl0ldProtect_3.b = *rdi_4
1401250f9     |         rdi_4 += 1
1401250fc     |         lpfl0ldProtect_1 = zx.q(lpfl0ldProtect_3)
1401250fc
1401250fe     if (lpfl0ldProtect_1.d == 0)
1401250fe     |         break
1401250fe
140125102     if (lpfl0ldProtect_1.b u> 0xef)
140125115     |         lpfl0ldProtect_1.b &= 0xf
14012511a     |         lpfl0ldProtect_1.w = *rdi_4
14012511d     |         rdi_4 += 2
14012511d
140125104     rbx_4 += lpfl0ldProtect_1
140125110     *rbx_4 = _bswap(*rbx_4) + lpfl0ldProtect_2
140125110
140125136     lpfl0ldProtect = lpfl0ldProtect_1
14012514a     void* lpAddress = VirtualProtect(
14012514a     |         lpAddress: lpfl0ldProtect_2 - 0x1000, dwSize: 0x1000,
14012514a     |         flNewProtect: PAGE_READWRITE, &lpfl0ldProtect)
140125153     *(lpAddress + 0x1a7) &= 0x7f
140125156     *(lpAddress + 0x1cf) &= 0x7f
140125168     VirtualProtect(lpAddress, dwSize: 0x1000,
140125168     |         flNewProtect: lpfl0ldProtect.d, &lpfl0ldProtect)
140125171     void* i_1 = &arg_30
14012517c     void var_50
14012517c
14012517c     do
140125177     |         *(i_1 - 8) = 0
140125177     |         i_1 -= 8
14012517c     while (i_1 != &var_50)
14012517c
140125182     ? 140125182     jump(&unpacked_code_UPX0)
140125182

```

Now we come to the last data block of the unpacking code. If the code detects that patching is necessary, it performs a series of operations to directly modify specific regions of memory. So this last block of code will do:

1. A loop walks through a section of memory, identifying and correcting relative addresses.
2. Some instructions use [_bswap](#) to reverse the order of bytes.
3. A set of subtractions adjusts obfuscated values to restore the correct bytes of the original code.
4. Calls to [VirtualProtect](#) are made to change the memory permissions, ensuring that the modifications are applied.
5. The transferred code is cleared and prepared for execution in a region now filled with unpacked code, in the **UPX0** section. Specifically, the address will be offset **0x140013a9f (unpacked_code_UPX0)**. And this offset is the entry point for the unpacked *Lockbit4.0*.

This last loop in which **VirtualProtect** is called and the program flow is unconditionally changed to the unpacked code, is clearly observed in the graphical format of the *Disassembly* below.

```

140125123 488b2d160f0000 mov rbp, qword [rel VirtualProtect]
14012512a 488dbe00f0ffff lea rdi, [rsi-0x1000]
140125131 bb00100000 mov ebx, 0x1000
140125136 50 push rax {!pf10ldProtect}
140125137 4989e1 mov r9, rsp {!pf10ldProtect}
14012513a 41b804000000 mov r8d, 0x4
140125140 53 push rbx {__return_addr} {0x1000}
140125141 5a pop rdx {__return_addr} {0x1000}
140125142 90 nop
140125143 57 push rdi {__return_addr}
140125144 59 pop rcx {__return_addr}
140125145 90 nop
140125146 4883ec20 sub rsp, 0x20
14012514a ffd5 call rbp
14012514c 488d87a7010000 lea rax, [rdi+0x1a7]
140125153 80207f and byte [rax], 0x7f
140125156 8060287f and byte [rax+0x28], 0x7f
14012515a 4c8d4c2420 lea r9, [rsp+0x20 {!pf10ldProtect}]
14012515f 4d8b01 mov r8, qword [r9 {!pf10ldProtect}]
140125162 53 push rbx {0x1000}
140125163 90 nop
140125164 5a pop rdx {0x1000}
140125165 90 nop
140125166 57 push rdi {var_20}
140125167 59 pop rcx {var_20}
140125168 ffd5 call rbp
14012516a 4883c428 add rsp, 0x28
14012516e 5d pop rbp {arg7}
14012516f 5f pop rdi {arg8}
140125170 5e pop rsi {arg9}
140125171 5b pop rbx {arg10}
140125172 488d442400 lea rax, [rsp-0x80 {var_50}]

140125177 6a00 push 0x0
140125179 4839c4 cmp rsp, rax {var_50}
14012517c 75f9 jne 0x140125177

14012517e 4883ec80 sub rsp, 0xffffffffffff80
140125182 e918e9e9ff jmp unpacked_code_UPX0
    
```

And below you can see that the region at offset **0x140013a9f** is in fact statically empty.

```

0x140013a9f UPX0 {0x140001000-0x140119000} Default
140013a9f unpacked_code_UPX0:
140013a9f 00
140013aa0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013ab0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013ac0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013ad0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013ae0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013af0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b00 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b10 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b20 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b30 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b40 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b50 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b60 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b70 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b80 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013b90 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013ba0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
140013bb0 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 .....
    
```

Now let's analyze this algorithm dynamically, with the aim of extracting the unpacked code from Lockbit4.0. Below we can see the exact space still empty, before the unpacking process.

Assembly code snippet:

```

0000000140125146 48:83EC 20      sub rsp,20
000000014012514A FFD5          call rbp
000000014012514C 48:8D87 A7010000 lea rax,qword ptr ds:[rdi+1A7]
0000000140125153 8020 7F      and byte ptr ds:[rax],7F
0000000140125156 8060 28 7F   and byte ptr ds:[rax+28],7F
000000014012515A 4c:8B4C24 20 lea r9,qword ptr ss:[rsp+20]
000000014012515F 4D:8B01      mov r8,qword ptr ds:[r9]
0000000140125162 53          push rbx
0000000140125163 90          nop
0000000140125164 5A          pop rdx
0000000140125165 90          nop
0000000140125166 57          push rdi
0000000140125167 59          pop rcx
0000000140125168 FFD5          call rbp
000000014012516A 48:83C4 28   add rsp,28
000000014012516E 5D          pop rbp
000000014012516F 5F          pop rdi
0000000140125170 5E          pop rsi
0000000140125171 5B          pop rbx
0000000140125172 48:8D4424 80 lea rax,qword ptr ss:[rsp-80]
0000000140125177 6A 00      push 0
0000000140125179 48:39C4     cmp rsp,rax
000000014012517C 75 F9      jne lockbit4.0.140125177
000000014012517E 48:83EC 80   sub rsp,FFFFFFFFFFFFFF80
0000000140125182 E9 18E9EFFF jmp lockbit4.0.140013A9F
0000000140125187 0000      add byte ptr ds:[rax],al
0000000140125189 0000      add byte ptr ds:[rax],al
    
```

Memory dump snippet:

Address	Hex	ASCII
0000000140013A9F	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000000140013AAF	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000000140013ABF	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000000140013ACF	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000000140013ADF	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0000000140013AFE	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

Jump is not taken
lockbit4.0.0000000140125177

UIPX1:000000014012517C lockbit4.0.exe:\$12517C #C37C

After the unpacking process is complete, the previously empty space is now filled with the unpacked code.

Assembly code snippet:

```

0000000140125164 5A          pop rdx
0000000140125165 90          nop
0000000140125166 57          push rdi
0000000140125167 59          pop rcx
0000000140125168 FFD5          call rbp
000000014012516A 48:83C4 28   add rsp,28
000000014012516E 5D          pop rbp
000000014012516F 5F          pop rdi
0000000140125170 5E          pop rsi
0000000140125171 5B          pop rbx
0000000140125172 48:8D4424 80 lea rax,qword ptr ss:[rsp-80]
0000000140125177 6A 00      push 0
0000000140125179 48:39C4     cmp rsp,rax
000000014012517C 75 F9      jne lockbit4.0.140125177
RIP -> 0000000140125181 48:83EC 80   sub rsp,FFFFFFFFFFFFFF80
0000000140125182 E9 18E9EFFF jmp lockbit4.0.140013A9F
0000000140125187 0000      add byte ptr ds:[rax],al
0000000140125189 0000      add byte ptr ds:[rax],al
    
```

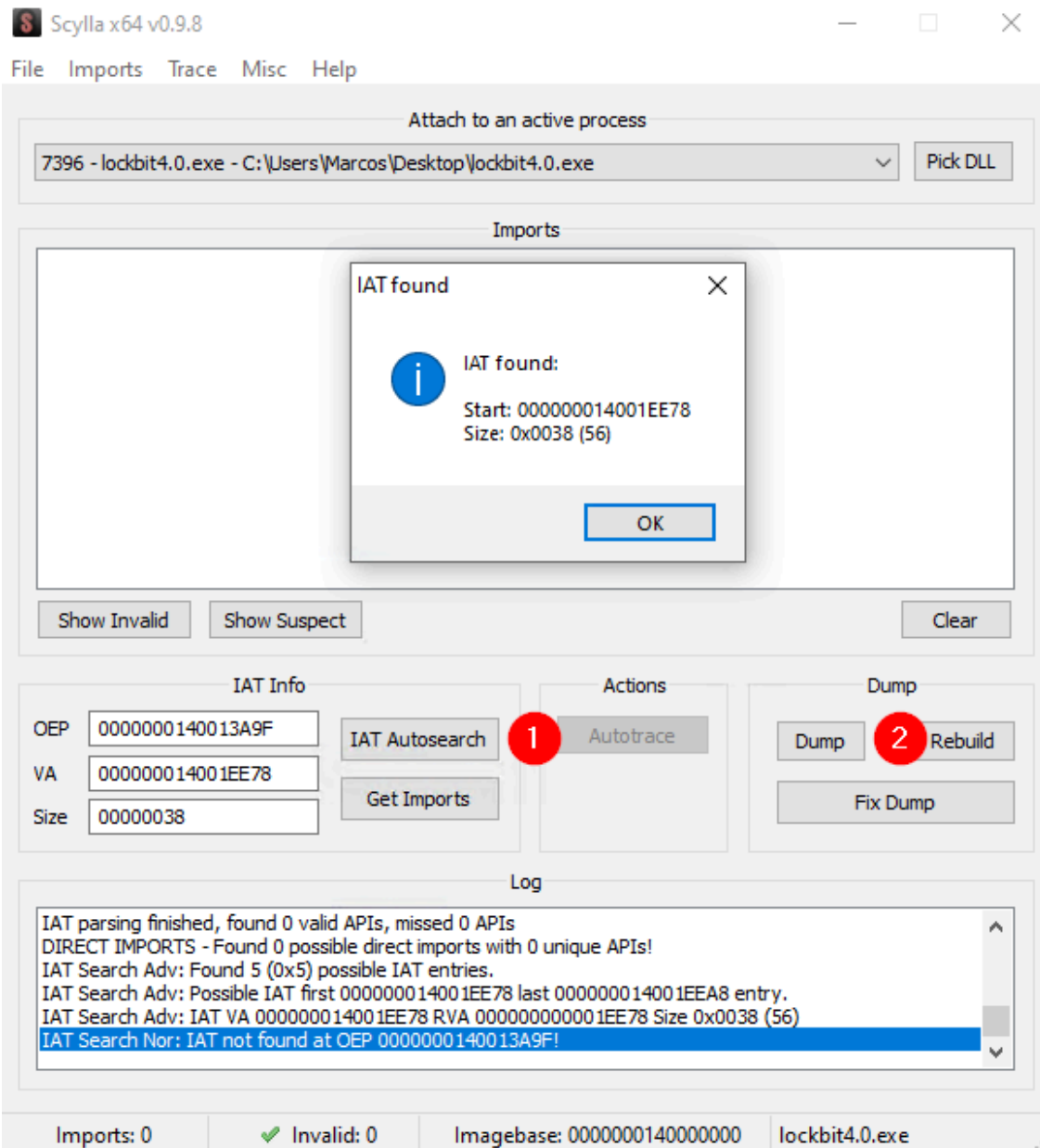
Memory dump snippet:

Address	Hex	ASCII
0000000140013A9F	41 57 41 56 41 55 41 54 56 57 55 53 48 81 EC 58	AWAVAUATVWUSH.1X
0000000140013AAF	09 00 00 0F 29 B4 24 40 09 00 00 48 8B 05 EF B3) \$@...H..T*
0000000140013ABF	00 00 48 85 C0 75 10 65 48 8B 04 25 60 00 00 00	..H.Au.eH.%....
0000000140013ACF	48 89 05 DA B3 00 00 48 8B 40 18 48 8B 78 20 48	H..0?..H@.H.x H
0000000140013ADF	8B 07 48 8B 18 48 8D B4 24 E0 03 00 00 90 56 59	..H..H.\$a...VY
0000000140013AEF	E8 94 10 FF FF 48 63 06 48 8B 04 03 48 89 44 24	è..yYHC.H...H.D\$
0000000140013AFF	68 48 89 05 61 AF 00 00 48 8B 3F 48 8D B4 24 E0	hh..a...H.?H.\$a
0000000140013B0F	03 00 00 90 56 59 E8 6E 10 FF FF 48 63 06 48 8B	...VYèn.yYHC.H.
0000000140013B1F	04 07 48 89 05 48 B3 00 00 BB B1 D1 57 6D BE 46	..H..H?..>Nwm%F

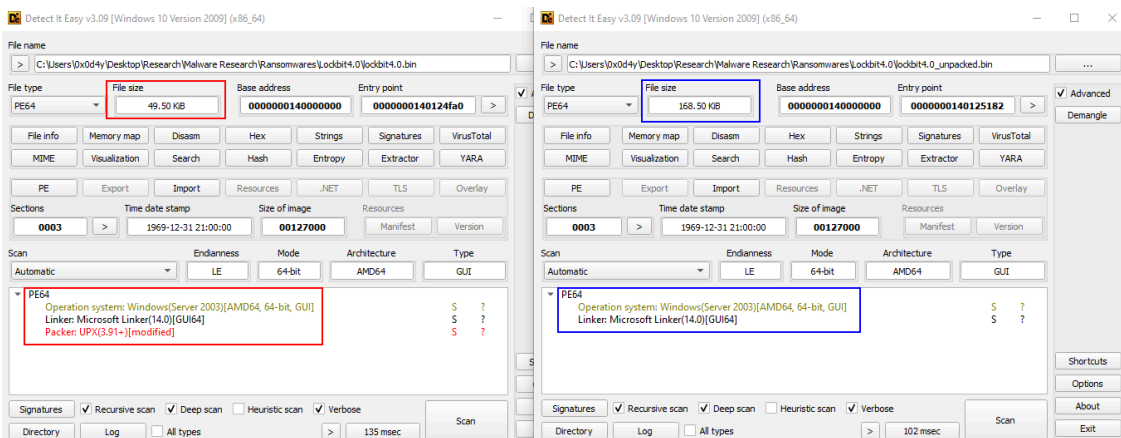
rsp=00000000014FEA8

UIPX1:000000014012517F lockbit4.0.exe:\$12517F #C37E

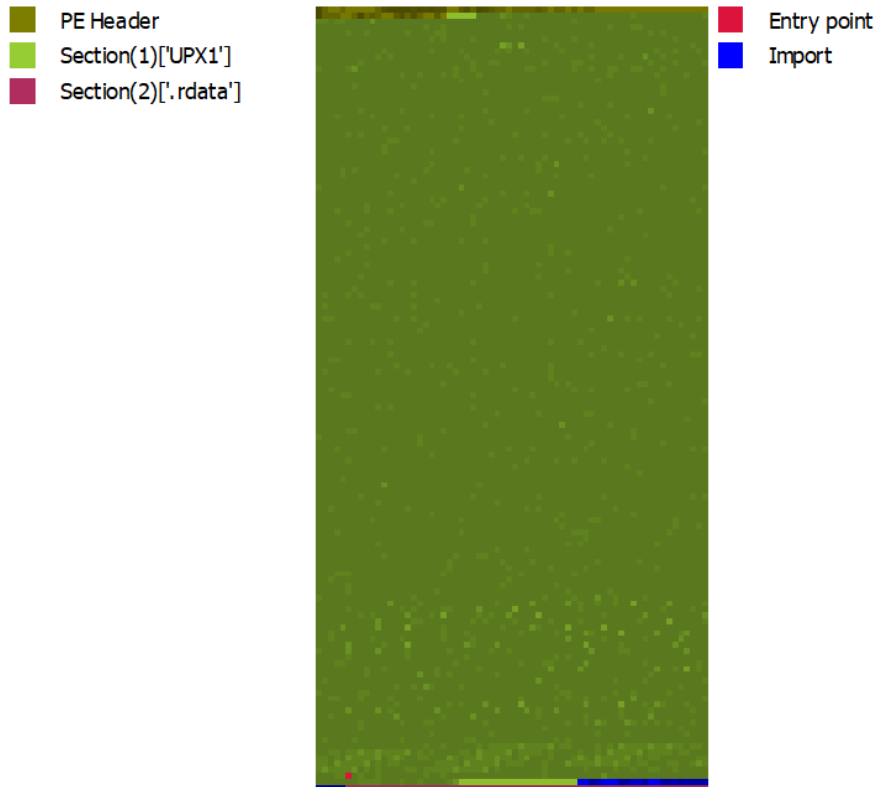
Once we reach this point, we will use the Scylla plugin to dump Lockbit4.0 unpacked.



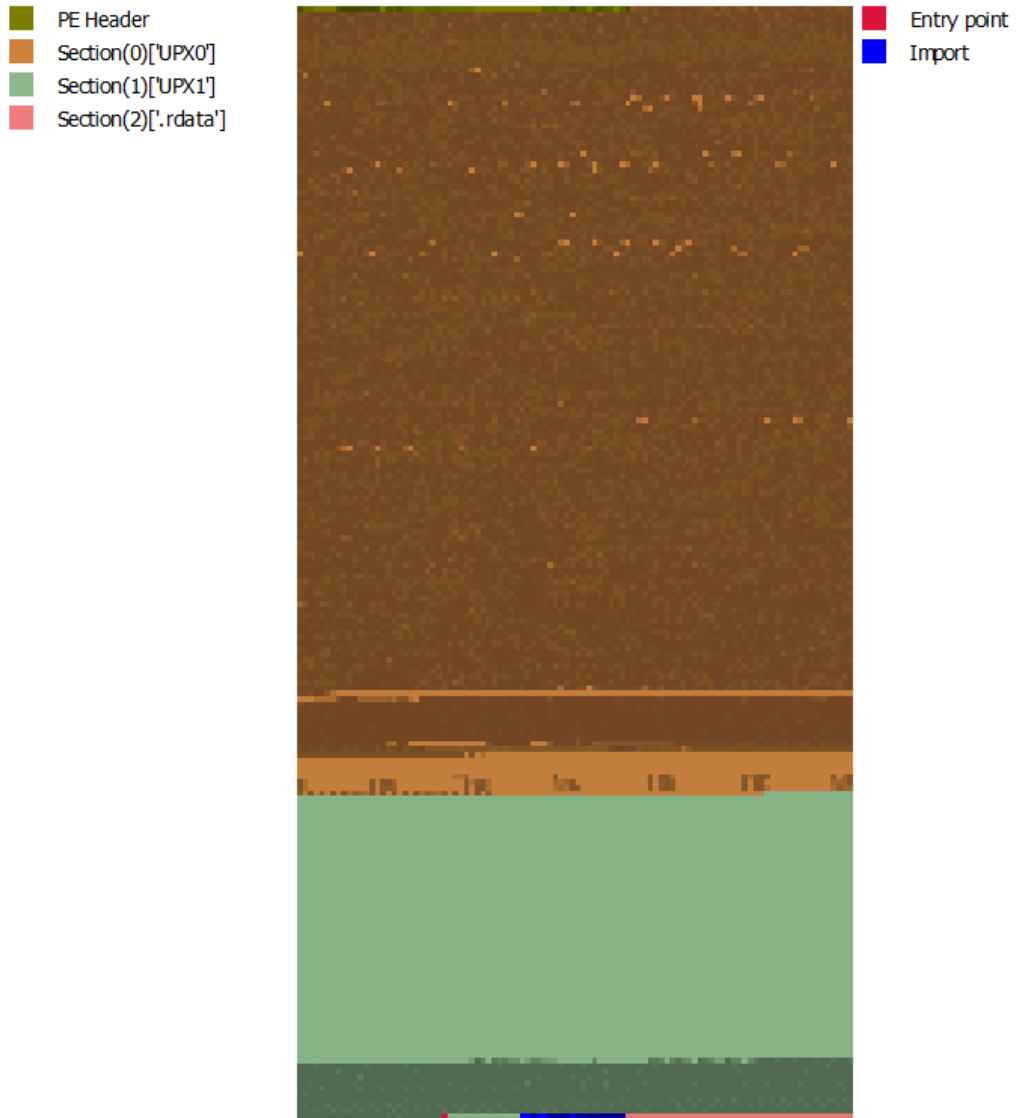
When comparing the original packed sample from Lockbit4.0 with the dynamically extracted unpacked version, we can observe a big difference in **DiE** size and detection.



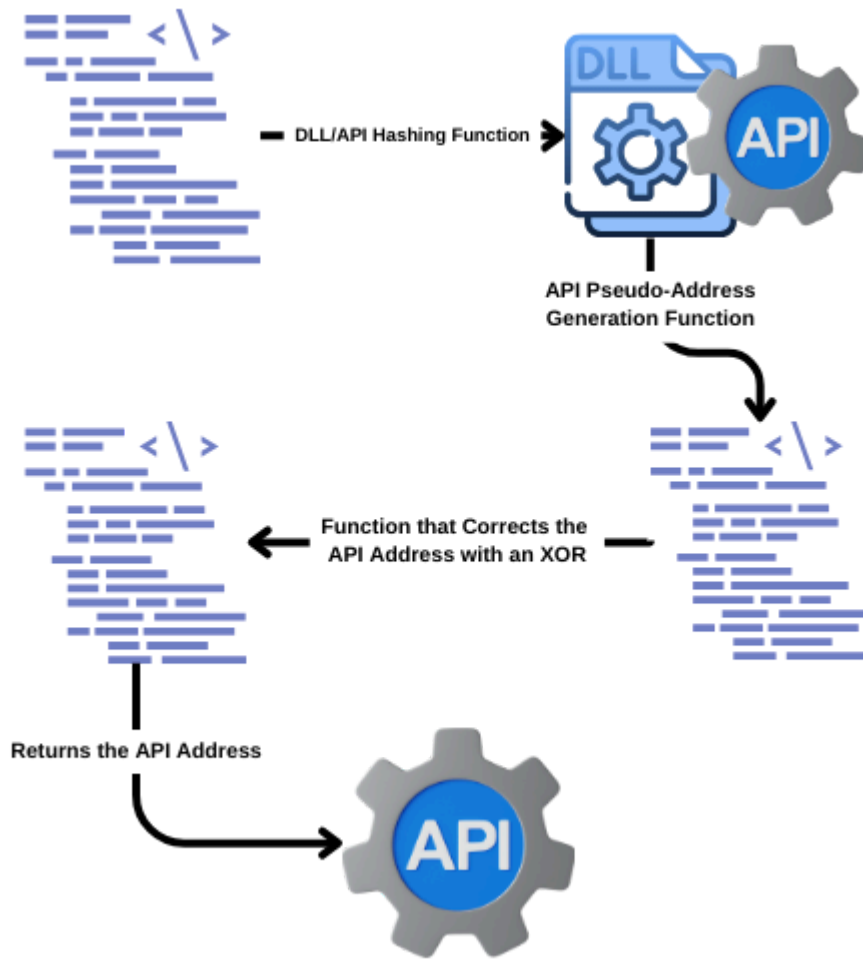
Below we can also see the difference in data organization in the packed version.



And below we can see the structural change of the unpacked version.



Despite the names of the sections being similar to the *IOCs* left by **UPX**, this is not a sample packed by UPX. And when we go to the offset where the unpacked code was written, we can see that it is filled with valid code, in this case the Lockbit4.0 Main function.



So, let's look first at DLL resolution via Hashing. Lockbit4.0's hashing algorithm is relatively easy, does not include any extra layers of obfuscation, and is intended to obfuscate DLLs that will be resolved at runtime. The algorithm traverses a data structure applying mathematical transformations and bitwise operations to generate an accumulative value (in the `rcx_17` variable).

```

int64_t lockbit4_hashing_resolution(int64_t arg1, int32_t arg2)
140004180 | int32_t rdx_8 = 0
140004182 | int32_t rcx_17 = 0x14bf
140004182 |
14000418a | while (true)
14000418a |     int32_t r8_2 = sx.d(
14000418a |         *(zx.q(*(rax_5 + (r14_2 << 2))) + hash_constant_1.q + zx.q(rdx_8)))
14000418a |
140004192 |     if (r8_2 == 0)
140004192 |         break
140004192 |
140004198 |     int32_t r10_1 = r8_2 + 0x20
140004198 |
1400041a0 |     if (r8_2.b - 0x41 u>= 0x1a)
1400041a0 |         r10_1 = r8_2
1400041a0 |
1400041b4 |     int32_t rcx_20 = rdx_8 ^ 0x14bf
1400041b4 |
1400041b8 |     if (rdx_8 == 0)
1400041b8 |         rcx_20 = rdx_8
1400041b8 |
1400041bf |     rcx_17 = rcx_20 * ((rdx_8 + 0x14bf) * r10_1 + (rcx_17 ^ r10_1)) + r10_1
1400041c2 |     rdx_8 += 1
1400041c2 |
1400041c6 |     r14_2 += 1
    
```

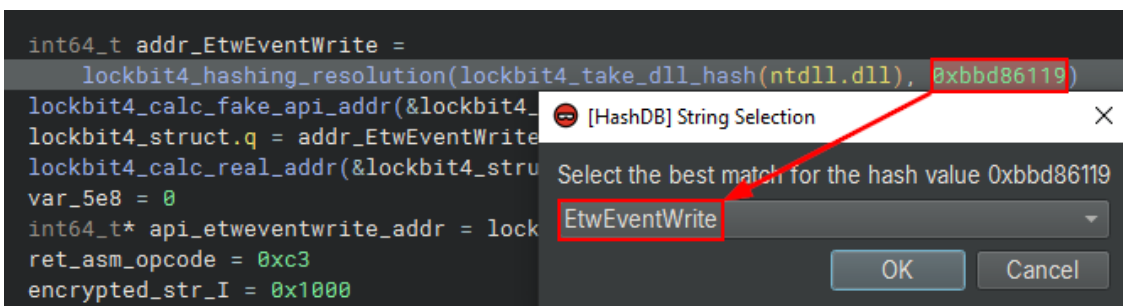
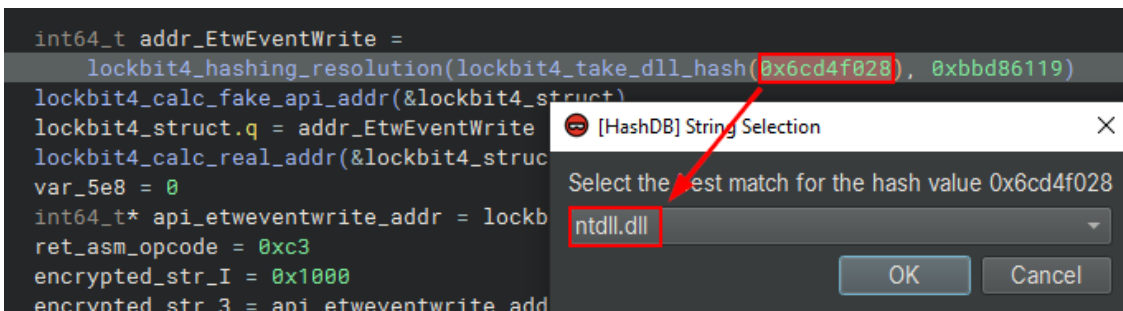
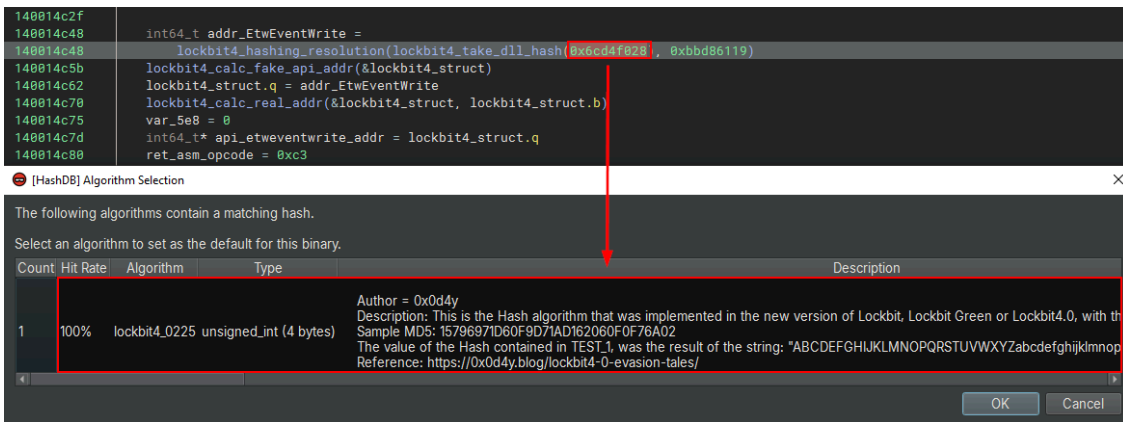
This hashing algorithm traverses a data structure applying mathematical transformations and bitwise operations to generate an accumulative value (**rcx_17**). Below is an objective summary of this algorithm:

1. **Initialization:** Defines variables, specifically in **rdx_8 = 0**, **rcx_17 = 0x14bf**.
2. **The Main Loop:** Reads values from memory indexed by **r14_2** and stops when it finds a *null value*.
3. **Conditional Conversion:** If the value is an uppercase letter (**A-Z**), converts it to lowercase (**+0x20**).
4. **Hash Calculation:** The algorithm will **XORs** in **rdx_8 ^ 0x14bf**, to ensuring variation in values.
5. **Hashing or Checksum:** Multiplies and combines values with **XOR** to create a cumulative identifier.
6. **Iteration:** It will increment **rdx_8** and **r14_2**, advancing to the next data block.

Below we can see the *Python* algorithm that I developed, which is already available in [HashDB](#) for automatic resolution, through the plugin available for **Ghidra**, **IDA** and **Binary Ninja**.

```
def lockbit4_hashing(hashing):  
  
    MASK_32BIT = 0xffffffff  
    hash_value = 0x14bf  
    char_index = 0  
  
    for char in hashing:  
        char_code = ord(char)  
  
        if 0x41 <= char_code <= 0x5A:  
            normalized_char = (char_code + 0x20) & MASK_32BIT  
        else:  
            normalized_char = char_code  
  
        if char_index == 0:  
            index_modifier = 0  
        else:  
            index_modifier = (char_index ^ 0x14bf) & MASK_32BIT  
  
        hash_value = (index_modifier * (((char_index + 0x14bf) * normalized_char + (hash_value ^ normalized_char  
  
        char_index += 1  
  
    return hash_value
```

In the following sequence of images, we can observe the use of HashDB for resolving DLL/API Hashing in Lockbit4.0.



A good example of the Hashing resolution process flow is the code below from Lockbit4.0, where we first see the resolution of the `ntdll.dll` Hash and the collection of its offset, followed by the resolution of the `EtwEventWrite` API Hash, storing them in a Lockbit4.0 custom structure. This piece of code is the beginning of the execution of the `ETW Patching` technique, where Lockbit4.0 will collect the address of the `EtwEventWrite` API, to overwrite the initial API code for the `ret` opcode (`0xc3`), thus applying the patch.

```
int64_t addr_EtwEventWrite =
    lockbit4_hashing_resolution(lockbit4_take_a_dll_hash(0x6cd4f028), 0xbbd86119)
lockbit4_calc_fake_api_addr(&lockbit4_struct)
lockbit4_struct.q = addr_EtwEventWrite
lockbit4_calc_real_addr(&lockbit4_struct, lockbit4_struct.b)
var_5e8 = 0
int64_t* etw_event_write_addr = lockbit4_struct.q
ret_opcode = 0xc3
```

As we can see above, after the resolution, we can see that a function is executed that *calculates a fake API address* through the `lockbit4_calc_fake_api_addr` function, and then the real address is calculated and stored once again in the Lockbit4.0 custom struct. Below, we can see that the calculation for the correct resolution of the API address is a simple `XOR` operation, with values present within the Lockbit4.0 custom struct.


```

// Vector Initialization Phase from 0 to 255
for (int64_t rc4_ksa_idx = 0; rc4_ksa_idx != 256; rc4_ksa_idx += 1)
    *(&lockbit4_struct + rc4_ksa_idx) = rc4_ksa_idx.b

int64_t rc4_prnga_idx = 0
char r10_3 = 0

// KSA Phase of RC4 Algorithm
for (; rc4_prnga_idx != 256; rc4_prnga_idx += 1)
    char r11_4 = *(&lockbit4_struct + rc4_prnga_idx)
    r10_3 =
        r10_3 + r11_4 + *(zx.q(mods.dp.d(sx.q(rc4_prnga_idx.d), 0x10)) + &rc4_key)
    uint64_t rax_168 = zx.q(r10_3)
    uint64_t rdx_91
    rdx_91.b = *(&lockbit4_struct + rax_168)
    *(&lockbit4_struct + rc4_prnga_idx) = rdx_91.b
    *(&lockbit4_struct + rax_168) = r11_4

int64_t rc4_data_decrypt_idx = 0
char* encrypted_readme = data_14001eeb8
char rdx_92 = 0
uint64_t idx = 0

// PRGA and RC4 Algorithm Decryption Phase
for (; rc4_data_decrypt_idx != 0x1853; rc4_data_decrypt_idx += 1)
    idx = zx.q(idx.b + 1)
    void* rc4_key
    rc4_key.b = *(&lockbit4_struct + idx)
    rdx_92 += rc4_key.b
    uint64_t r10_4 = zx.q(rdx_92)
    *(&lockbit4_struct + idx) = *(&lockbit4_struct + r10_4)
    *(&lockbit4_struct + r10_4) = rc4_key.b
    rc4_key.b += *(&lockbit4_struct + idx)
    rc4_key.b = *(&lockbit4_struct + zx.q(rc4_key.b))
    encrypted_readme[rc4_data_decrypt_idx] ^= rc4_key.b
    
```

Without any extra obfuscation layers, we are able to identify the RC4 key and the encrypted README.

```

14001cb70 rc4_key:
14001cb70 ca 7e 7b 2b 60 8c 2d 32-31 03 b7 7e d4 9e 1f 8e
14001cb80 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00 00 .....~{+~.-21..~....

14001b310 encrypted_readme:
14001b310 e6 6e 22 1d 66 b2 0c 12-e7 c7 cd a4 47 b3 c4 53 .....n".f.....G..S
14001b320 bf b7 84 d8 2a 8f d0 2b-42 19 08 d8 99 e2 44 df-e8 06 4c a8 0b 13 08 57-25 e4 92 4a 8d 01 bd 76 ....*..+B....D...L...W%.J...v
14001b340 69 69 20 04 98 57 af 98-b3 81 ad 52 84 aa d0 b7-36 a3 9e 2a cf 28 30 eb-4a f6 28 85 fc b4 01 3d ..i..W....R....6..*(0.J.(...=
14001b360 85 ee d6 e4 0e 40 c9 88-85 9b 46 ca 77 92 30 52-b7 79 56 70 71 71 8b ac-a3 26 39 1d b6 5d 6e 1b .....@....F..w.0R.yVpqq...&9..]n.
14001b380 e5 2b 32 29 71 14 44 79-83 c8 39 47 85 ad 1d d1-2b 3a 07 6f 7a 71 74 74-7e 0e 17 1d 2d 1a 61 d8 ...+2)q.Dy..9G...+;.ozqtt-...-a.
14001b3a0 c4 6c 27 d0 a4 59 28 0a-b6 67 76 33 25 e0 52 8e-a8 f0 7b 76 85 79 68 1e-b4 a2 33 12 45 08 df b5 ..l'..Y(...gv3%.R...{v.yh...3.E...
14001b3c0 ec c2 0f 11 0e 71 bc 27-ee 73 f8 18 2c dd 88 fd-d6 45 8d 80 f3 2c 0d 9b-f3 61 3e 20 13 88 da c2 .....q'.s...mE.....a> ...
14001b3e0 81 24 1d 88 5b 81 5f b2-c9 45 1f f7 03 a8 ae 02-fc 3e 4a 6e c5 46 c3 44-e0 2a 67 02 f1 b7 9a 25 $ [ E ...>ln.F.D.#g...
    
```

Since it is a well-known algorithm, and widely used by Malware, it is easy to implement this algorithm in Python. Below is my implementation, followed by the output of its execution (*I removed the values of the RC4 key and the large block of data from the encrypted README, to keep the visual appearance cleaner*).

```

def rc4(key: bytes, data: bytes) -> bytes:
    # KSA Phase
    S = list(range(256))
    j = 0
    key_length = len(key)
    
```

```
# PRGA Phase
for i in range(256):
    j = (j + S[i] + key[i % key_length]) % 256
    S[i], S[j] = S[j], S[i]

# Decryption Phase
i = 0
j = 0
result = bytearray()
for byte in data:
    i = (i + 1) % 256
    j = (j + S[i]) % 256
    S[i], S[j] = S[j], S[i]
    K = S[(S[i] + S[j]) % 256]
    result.append(byte ^ K)

return bytes(result)

if __name__ == "__main__":
    rc4_key = "RC4_KEY"

    encrypted_lb4_readme = (
        "ENCRYPTED_README_DATA"
    )

    rc4_key_bytes = bytes.fromhex(rc4_key)
    encrypted_readme_bytes = bytes.fromhex(encrypted_lb4_readme)

    decrypted_bytes = rc4(rc4_key_bytes, encrypted_readme_bytes)

    try:
        decrypted_lb4_readme = decrypted_bytes.decode("utf-8")
    except UnicodeDecodeError:
        decrypted_lb4_readme = decrypted_bytes.decode("latin1", errors="replace")

    print("\nLockbit4.0 Decrypted Readme:")
    print(decrypted_lb4_readme)
```



```
description = "Detect the implementation of RC4 Algorithm by Lockbit4.0"
date = "2024-02-13"
score = 100
yarahub_reference_md5 = "062311F136D83F64497FD81297360CD4"
yarahub_uuid = "4de48ced-b9fa-4286-aac4-c263ad20d67d"
yarahub_license = "CC BY 4.0"
yarahub_rule_matching_tlp = "TLP:WHITE"
yarahub_rule_sharing_tlp = "TLP:WHITE"
malpedia_family = "win.lockbit"
strings:
    $rc4_alg = { 48 3d 00 01 00 00 74 0c 88 84 04 ?? ?? ?? ?? 48 ff c0 eb ec 29 c9 41 b8 ?? ?? ?? ?? 4c 8d (

condition:
    uint16(0) == 0x5a4d and
    $rc4_alg
}
```

```
rule lb4_hashing_alg
{
    meta:
        author = "0x0d4y"
        description = "This rule detects the custom hashing algorithm of Lockbit4.0 unpacked"
        date = "2024-02-16"
        score = 100
        yarahunb_reference_md5 = "062311F136D83F64497FD81297360CD4"
        yarahunb_uuid = "d1a6d555-626d-4625-9da6-e4478cb7a142"
        yarahunb_license = "CC BY 4.0"
        yarahunb_rule_matching_tlp = "TLP:WHITE"
        yarahunb_rule_sharing_tlp = "TLP:WHITE"
        malpedia_family = "win.lockbit"
    strings:
        $hashing_alg = { 41 89 d0 46 0f be 04 00 45 09 c0 74 ?? 45 8d 48 ?? 45 8d 50 ?? 41 80 f9 ?? 45 0f 43 d0

    condition:
        uint16(0) == 0x5a4d and
        $hashing_alg
}
```

Detection Engineering - Yara Hunts

With the YARA rules produced, I carried out a Yara Hunt on UnpacMe and below is the link shared with the matches produced by the Hunt with the YARA rules above.

- [lb4 packer was detected](#) ;
- [lb4 rc4 alg](#) ;

- [lb4 hashing_alg.](#)
-

Conclusion

Throughout the analysis, Lockbit4.0 presents us with a version that is much more concerned with implementing *Obfuscation* techniques, such as the *DLL/API Hashing* technique and the *DLL/API* address resolution technique divided into phases, with the clear purpose of obfuscating its intentions and slowing down the analysis. And we can also observe its concern with implementing Endpoint Protection Software Evasion techniques, through techniques such as *ETW Patching* and *Disabling DLL Loading Notifications*. In addition, it is also possible to observe the introduction of the network enumeration technique in an autonomous manner, through the collection of IP addresses from the *ARP Table* and the *Routing Table*, through the IPs mentioned in the research. There is no secret in the implementation of this technique, since it is entirely done through the use of Windows APIs, with the only layer of complexity being the implementation of the *DLL/API* resolution technique dynamically.

Therefore, unlike the previous version, this new version of Lockbit ransomware is focused on staying under the radar.

Source: <https://0x0d4y.blog/lockbit4-0-evasion-tes/>